

C7Z17O-SQ USER'S MANUAL

Revision 1.0b

The information in this User's Manual has been carefully reviewed and is believed to be accurate. The vendor assumes no responsibility for any inaccuracies that may be contained in this document, makes no commitment to update or to keep current the information in this manual, or to notify any person or organization of the updates. **Please Note: For the most up-to-date version of this manual, please see our web site at www.supermicro.com.**

Super Micro Computer, Inc. ("Supermicro") reserves the right to make changes to the product described in this manual at any time and without notice. This product, including software and documentation, is the property of Supermicro and/or its licensors, and is supplied only under a license. Any use or reproduction of this product is not allowed, except as expressly permitted by the terms of said license.

IN NO EVENT WILL SUPERMICRO BE LIABLE FOR DIRECT, INDIRECT, SPECIAL, INCIDEN-TAL, SPECULATIVE OR CONSEQUENTIAL DAMAGES ARISING FROM THE USE OR INABILITY TO USE THIS PRODUCT OR DOCUMENTATION, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. IN PARTICULAR, SUPERMICRO SHALL NOT HAVE LIABILITY FOR ANY HARDWARE, SOFTWARE, OR DATA STORED OR USED WITH THE PRODUCT, INCLUDING THE COSTS OF REPAIRING, REPLACING, INTEGRATING, INSTALLING OR RECOVERING SUCH HARDWARE, SOFTWARE, OR DATA.

Any disputes arising between manufacturer and customer shall be governed by the laws of Santa Clara County in the State of California, USA. The State of California, County of Santa Clara shall be the exclusive venue for the resolution of any such disputes. Super Micro's total liability for all claims will not exceed the price paid for the hardware product.

FCC Statement: This equipment has been tested and found to comply with the limits for a class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment to an outlet on a circuit different from that to which the receiver is connected.
- Consult the authorized dealer or an experienced radio/TV technician for help.

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. "Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate"

WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

Manual Revision 1.0b Release Date: January 19, 2016

Unless you request and receive written permission from Super Micro Computer, Inc., you may not copy any part of this document.

Information in this document is subject to change without notice. Other products and companies referred to herein are trademarks or registered trademarks of their respective companies or mark holders.

Copyright \circledast 2016 by Super Micro Computer, Inc. All rights reserved. Printed in the United States of America

Preface

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the **SUPER**• C7Z170-SQ motherboard.

Manual Organization

Chapter 1 describes the features, specifications and performance of the motherboard, and provides detailed information on the Intel Z170 Express chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system.

If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for video, memory and system setup stored in the CMOS.

Chapter 4 includes an introduction to the BIOS, and provides detailed information on running the CMOS Setup utility.

Appendix A provides BIOS Error Beep Codes.

Appendix B lists software program installation instructions.

Appendix C contains UEFI BIOS Recovery instructions.

Appendix D contains an introduction and instructions regarding the Dual Boot Block feature of this motherboard.

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

Please check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

The following items are included in the retail box.

- One (1) Supermicro Motherboard
- Six (6) SATA cables
- One (1) I/O shield
- One (1) Quick Reference Guide
- One (1) Driver CD

Conventions Used in the Manual

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:



Attention! Critical information to prevent damage to the components or injury to yourself.



Important: Important information given to ensure proper system installation or to relay safety precautions.



Note: Additional Information given to differentiate various models or provides information for correct system setup.

Standardized Warning Statements

The following statements are industry-standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components in the Supermicro chassis.

Battery Handling



Warning!

There is a danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。 交換する電池はメー カーが推奨する型、または同等のものを使用下さい。 使用済電池は製造元の指示に従 って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有 電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

iAdvertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה ! קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת.

סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة استبدال البطارية بطريقة غير صحيحة فعليك استبدال البطارية فقط بنفس النوع أو ما يعادلها كما أوصت به الشركة المصنعة تخلص من البطاريات المستعملة وفقا لتعليمات الشركة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일 하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사 의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Product Disposal



Ultimate disposal of this product should be handled according to all national laws and regulations. 製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要が あります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

iAdvertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignezvous auprès de l'organisme compétent.

סילוק המוצר

אזהרה ! סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

Contacting Supermicro

Headquarters

Address:	Super Micro Computer, Inc.		
	980 Rock Ave.		
	San Jose, CA 95131 U.S.A.		
Tel:	+1 (408) 503-8000		
Fax:	+1 (408) 503-8008		
Email:	marketing@supermicro.com (General Information)		
	support@supermicro.com (Technical Support)		
Web Site:	www.supermicro.com		
Europe			
Address:	Super Micro Computer B.V.		
///////////////////////////////////////	Het Sterrenbeeld 28, 5215 ML		
	's-Hertogenbosch, The Netherlands		
Tel:	+31 (0) 73-6400390		
Fax:	+31 (0) 73-6416525		
Email:			
Email: sales@supermicro.nl (General Information) support@supermicro.nl (Technical Support)			
	rma@supermicro.nl (Customer Support)		
Web Site:	www.supermicro.nl		
Web Site.	www.supermero.m		
Asia-Pacific			
Address:	Super Micro Computer, Inc.		
	3F, No. 150, Jian 1st Rd.		
	Zhonghe Dist., New Taipei City 235		
	Taiwan (R.O.C)		
Tel:	+886-(2) 8226-3990		
Fax:	+886-(2) 8226-3992		

- Email: support@supermicro.com.tw
- Web Site: www.supermicro.com.tw

Where to Find More Information

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your motherboard.

SMCI product manuals: http://www.supermicro.com/support/manuals/

Product Drivers and utilities: ftp://ftp.supermicro.com/

If you have any questions, please contact our support team at support@ supermicro.com.

Table of Contents

Preface

Manu	ual Organizationiii
Chec	:klistiv
Conv	ventions Used in the Manualiv
Stan	dardized Warning Statements v
	Battery Handlingv
	Product Disposalvi
Cont	acting Supermicroviii
Whe	re to Find More Informationix
Cha	pter 1 Introduction
1-1	Overview
	About this Motherboard1-1
1-2	Chipset Overview1-1
	Intel Z170 Express Chipset Features 1-1
1-3	Motherboard Features 1-2
1-4	Special Features 1-4
	Recovery from AC Power Loss 1-4
1-5	PC Health Monitoring1-4
	Fan Status Monitor with Firmware Control1-4
	Environmental Temperature Control1-4
	System Resource Alert 1-5
1-6	ACPI Features1-5
	Slow Blinking LED for Suspend-State Indicator1-5
1-7	Power Supply1-6
1-8	Super I/O 1-6
Cha	pter 2 Installation
2-1	Installation Components and Tools Needed
2-2	Static-Sensitive Devices2-2
	Precautions 2-2
	Unpacking2-2
2-3	Processor and Heatsink Installation2-3
	Installing the LGA1151 Processor2-3
	Installing an Active CPU Heatsink with Fan2-6
	Removing the Heatsink2-8
2-4	Installing DDR4 Memory 2-9
	DIMM Installation2-9
	Removing Memory Modules2-9

	Memory Support	2-10
	Memory Population Guidelines	2-11
2-5	Motherboard Installation	2-12
	Tools Needed	2-12
	Location of Mounting Holes	2-12
	Installing the Motherboard	2-13
2-6	Connectors/IO Ports	2-14
	Back I/O Panel	2-14
	Universal Serial Bus (USB)	2-15
	Ethernet Port	2-16
	Back Panel High Definition Audio (HD Audio)	2-16
	S/PDIF Port	2-17
	VESA® DisplayPort [™]	2-17
	HDMI Port	2-17
	DVI-D Port	2-17
	Front Control Panel	2-18
	Front Control Panel Pin Definitions	2-19
	Power LED	2-19
	HDD LED	2-19
	NIC1 (LAN)	2-19
	Overheat (OH)/Fan Fail	2-19
	Reset Button	2-20
	Power Button	2-20
2-7	Connecting Cables	2-21
	ATX Main PWR & CPU PWR Connectors (JPW1 & JPW2)	2-21
	Fan Headers (Fan 1 ~ Fan 5)	2-22
	Chassis Intrusion (JL1)	2-22
	Internal Buzzer (SP1)	2-23
	Speaker (JD1)	2-23
	Onboard Power LED (JLED1)	2-24
	Serial Port (COM1)	2-24
	DOM PWR Connector (JSD1)	2-25
	SPDIF OUT (JSPDIF_OUT)	2-25
	Standby Power Header (STBY1)	2-26
	PCI-E M.2 Connector (PCI-E M.2)	2-26
	Front Panel Audio Header (AUDIO FP)	2-27
	OC Front Panel (OC FRONT PANEL)	2-27
2-8	Jumper Settings	2-28
	Explanation of Jumpers	2-28

	LAN Enable/Disable 2-28
	Clear CMOS & JBT1 2-29
	PCI Slot SMB Enable (I ² C1/I ² C2)2-29
	Audio Enable (JPAC1) 2-30
	Watch Dog Enable/Disable 2-30
	USB Wake Up (JPUSB1)
	Manufacturing Mode (JPME2)2-31
	BIOS Recovery Switch (JBR1) 2-32
	Power Button (POWER BUTTON)
	BIOS Restore (BIOS RESTORE) 2-32
2-9	Onboard Indicators 2-33
	LAN LEDs 2-33
	Onboard Power LED (LED1) 2-33
	Status Display (LED4)2-34
	M.2 On Board (LED2) 2-34
2-10	SATA Connections 2-35
	SATA Connections (I-SATA0~I-SATA5) 2-35
2-11	The OC Front Control Panel (Optional) 2-36
	Overclocking Buttons (OC1, OC2, OC3) 2-36
	Clear CMOS and Reset 2-36
	Boot BIOS Switch 2-36
	BIOS Recovery 2-36
	Front USB Port 2-36
	Installing the OC Front Control Panel 2-37
Chap	oter 3 Troubleshooting
3-1	Troubleshooting Procedures
	Before Power On 3-1
	No Power
	No Video 3-2
	Memory Errors 3-2
	When the System is Losing the Setup Configuration
3-2	Technical Support Procedures 3-3
3-3	Frequently Asked Questions 3-4
3-4	Battery Removal and Installation 3-5
	Battery Removal 3-5
	Proper Battery Disposal 3-5
3-5	Returning Motherboard for Service
	Battery Installation 3-6

Chapter 4 BIOS

4-1	Introduction
	Starting BIOS GUI Setup Utility 4-1
	How To Change the Configuration Data
	How to Start the Setup Utility 4-2
4-2	Saving and Loading
	Save and Load
	Restore Defaults
	Save All Settings Only 4-4
	Save Changes and Exit 4-4
	Save Changes and Reset
	Discard Changes
	Exit without Save4-4
	Discard Changes and Reset 4-4
	Profiles
	Save Profile 1 / Save Profile 2 4-5
	Load Profile 1 / Load Profile 2 4-5
4-3	System Information 4-6
	Motherboard
	System Date 4-6
	System Time
	CPU
	Memory
4-4	Processor/CPU
	Information
	Performance 4-10
	CPU Flex Ratio Override 4-10
	CPU Flex Ratio Settings 4-11
	Overclocking lock 4-11
	Intel® Virtualization Technology
	Hardware Prefetcher 4-11
	Adjacent Cache Line Prefetch 4-11
	CPU AES 4-11
	Boot Performance Mode 4-11
	HardWare P States (HWP)4-12
	Power Management 4-12
	Intel(R) SpeedStep(tm) 4-12
	Turbo Mode 4-12
	CPU C States 4-12

	Enhanced C-States 4-13
	C-State Auto Demotion 4-13
	C-State Un-demotion 4-13
	Package C-State Demotion 4-13
	Package C-State Un-Demotion 4-13
	Package C State limit 4-13
	CPU Thermal Configuration 4-13
4-5	Overclocking 4-14
	CPU Overclocking 4-14
	BCLK Clock Frequency(1/100 Mhz) 4-14
	FCLK Frequency for Early Power On
	Active Processor Cores 4-15
	Load SMC CPU OC Setting 4-15
	1-Core Ratio Limit Override 4-15
	2-Core Ratio Limit Override 4-15
	3-Core Ratio Limit Override
	4-Core Ratio Limit Override 4-15
	Boot Performance Mode 4-15
	Intel(R) SpeedStep(tm) Technology 4-16
	Turbo Mode 4-16
	Package Power Limit MSR Lock
	Power Limit 1 Override 4-16
	Power Limit 1
	Power Limit Window 4-16
	Power Limit 2 Override 4-16
	Power Limit 2 4-17
	Platform PL1 Enable
	Platform PL1 Power
	Platform PL1 Window 4-17
	Platform PL2 Enable 4-17
	Platform PL2 Power
	Power Limit 3 Override 4-17
	Power Limit 3
	Power Limit 3 Window 4-18
	Power Limit 3 Lock 4-18
	CPU Flex Ratio Override 4-18
	CPU Flex Ratio Settings 4-18
	CPU Max OC Ratio 4-19

	System Agent Voltage (mV)	4-19
	Core Voltage Mode	4-19
	Core Extra Turbo Voltage	4-19
	Core Voltage Offset (mV)	4-19
	Offset Prefix	4-19
	Core PLL Voltage Offset	4-19
	Ring Max OC Ratio	4-19
	Ring Min OC Ratio	4-19
	Ring Voltage Mode	4-20
	Ring Extra Turbo Voltage	4-20
	Ring Voltage Offset	4-20
	Offset Prefix	4-20
	Uncore Voltage Offset	4-20
	Offset Prefix	4-20
	PCH 1.0V	4-20
	VCCPLL_OC	4-20
	VCCPLL_OC	4-20
	Load Line Calibration	4-20
	VRM Total Current Limit Capability	4-21
	VRM Per-Phase Current Limit Capability	4-21
	PMON Slope	4-21
	PMON Offset	4-21
	PSYS PMax Current	4-21
	System Agent VR Settings	4-21
	Core/IA VR Settings	4-21
	Ring VR Settings	4-21
Me	emory Overclocking	4-22
	Load Memory Profiles	4-22
	Memory Timing Configuration (Expert Mode)	4-22
3r	d Timing:	4-26
Gr	aphics OverClocking	4-30
G٦	Slice Domain	4-30
	GT Voltage Mode	4-30
	GT Extra Turbo Voltage Target	4-30
	GT Voltage Offset	4-30
	Offset Prefix	4-31
G٦	Unslice Domain	4-31
	GT Voltage Mode	4-31
	GT Extra Turbo Voltage Target	4-31

	GT Voltage Offset	4-31
	Offset Prefix	4-31
4-6	Memory	4-32
	Memory Information	4-32
	Memory Configuration	4-33
	Maximum Memory Frequency	4-33
	Max TOLUD (Top of Low Usable DRAM)	4-33
	Memory Scrambler	4-34
	Fast Boot	4-34
4-7	Booting	4-34
	Boot Device Settings	4-34
	Quiet Boot	4-34
	Boot Mode Select	4-35
	Legacy Boot Order #1~#7	4-35
	Delete Driver Option	4-35
	NETWORK Drive BBS Priorities	4-35
	BIOS Features	4-35
	Setup Prompt Timeout	4-35
	Bootup Numlock State	4-35
	Wait for "F1" for Error	4-36
	Retry Boot	4-36
	Power Button Function	4-36
	AC Loss Policy	4-36
	EUP Support	4-36
4-8	Input/Output	4-37
	SATA	4-37
	SATA Controllers	4-37
	SATA Mode Selection	4-37
	SATA Controller Speed (AHCI or RAID Mode only)	4-37
	Port 0 ~ Port 5	4-37
	Hot Plug	4-38
	Spin Up Device	4-38
	SATA Device Type	4-38
	PCIe/PCI/PnP	4-38
	PEG Port Configuration	4-38
	PCH SLOT5 PCI-E 3.0 X1 (IN X4), PCH SLOT3 PCI-E 3.0 X4 PCI-E X4, PCH SLOT1 PCI-E 3.0 X1 (IN X4)	
	PCI Express Root Port 20	4-38
	ASPM Support	4-39

	L1 Substates 4-39
	PCIE Speed 4-39
	Detect Non-Compliant Device 4-39
	PCIE LTR Lock 4-39
	Option ROM Execution
	Video 4-39
	Storage Option ROM/UEFI Driver 4-39
	PCIe/PCI/PnP Configuration 4-39
	PCH SLOT1 PCI-E 3.0 X1 (IN X4) OPROM, CPU SLOT2 PCI-E 3.0 X4 (IN X16) OPROM, PCH SLOT3 PCI-E 3.0 X4 OPROM, CPU SLOT4 PCI-E 3.0 X8 (IN X16) OPROM, PCH SLOT5 PCI-E 3.0 X1 (IN X4) OPROM, CPU SLOT6 PCI-E 3.0 X16 OPROM
	Onboard LAN Option ROM Type 4-40
	Onboard LAN1 Option ROM 4-40
	Network Stack 4-40
	Ipv4 PXE Support (Available when Network Stack is set to Enabled) 4-40
	Ipv6 PXE Support (Available when Network Stack is set to Enabled)
	USB Settings
	Legacy USB Support 4-41
	EHCI Support 4-41
	XHCI Support 4-41
	USB 3.1 Support 4-41
	Install Windows 7 USB Support 4-41
	PCH-FW (Firmware) 4-42
	ME FW Image Re-Flash 4-42
	Super IO Configuration 4-43
	Serial Port 1 Configuration 4-43
	Serial Port 4-43
	Device Settings 4-43
	IRQ Settings 4-43
4-9	Hardware Monitor 4-44
	Temperature 4-44
	Fan Speed 4-45
	Fan Speed Control Mode 4-45
	Voltage 4-46
4-10	Management 4-47
	ACPI Settings 4-47

	ACPI Sleep State	4-47
	Console Redirection	4-48
	COM 1	4-48
	Console Redirection	4-48
	SuperFlash Utility	4-52
4-11	Chipset	4-56
	System Agent	4-56
	VT-d	4-56
	SW Guard Extensions (SGX)	4-56
	PRMRR Size	4-57
	Graphics	4-57
	RC6 (Render Standby)	4-57
	Primary Display	4-58
	Internal Graphics	4-58
	PCH-IO	4-58
	HD Audio	4-58
	PCH LAN Controller	4-59
	Wake on LAN	4-59
	PCIE PLL SSC	1-59
4-12	Security	1-59
	Password	4-59
	Administrator Password	4-60
	User Password	4-60
	Password Check	4-60
	Secure Boot Menu	4-60
	Secure Boot	4-61
	Secure Boot Mode	4-61
	Key Management	4-61
	Provision Factory Default Keys	4-62
	Enroll Factory Default Keys	1-62
	Platform Key	4-62
	Set New Key	4-62
	Delete Key	4-63
	Key Exchange Key	4-63
	Set New Key	4-63
	Append Key	4-63
	Delete Key	4-63
	Authorized Signatures	4-63

	Set New Key 4-63
	Append Key 4-64
	Delete Key 4-64
	Forbidded Signatures 4-64
	Delete Key 4-64
	Set New Key 4-64
	Append Key 4-64
	Authorized TimeStamps 4-64
	Set New Key 4-64
	Append Key 4-65
	Delete Key 4-65
4-13	Recovery 4-65
Арре	endix A BIOS Error Beep Codes
A-1	BIOS Error Beep Codes A-1
Арре	endix B Software Installation Instructions
B-1	Installing DriversB-1
B-2	Configuring SuperDoctor [®] IIIB-2
Арре	endix C UEFI BIOS Recovery Instructions
C-1	An Overview to the UEFI BIOS
C-2	How to Recover the UEFI BIOS Image (-the Main BIOS Block)C-1
C-3	To Recover the Main BIOS Block Using a USB-Attached DeviceC-2
Арре	endix D Dual Boot Block
D-1	IntroductionD-1
	BIOS Boot Block
	BIOS Boot Block Corruption OccurrenceD-1
D-2	Steps to Reboot the System by switch JBR1D-2

Notes

Chapter 1

Introduction

1-1 Overview

About this Motherboard

The C7Z170-SQ supports a single 6th Generation Intel® Core[™] i7/i5/i3 processors in an LGA 1151 (H4) socket. With the Intel® Z170 Express chipset built in, the C7Z170-SQ motherboard offers substantial system performance and storage capability for overclocking platforms in a sleek package. Please refer to our website (http://www.supermicro.com/prod-ucts/) for processor and memory support updates.

1-2 Chipset Overview

Intel Z170 Express Chipset Features

- Direct Media Interface (up 10 Gb/s transfer, Full Duplex)
- Intel® Matrix Storage Technology and Intel Rapid Storage Technology
- Dual NAND Interface
- Intel I/O Virtualization (VT-d) Support
- Intel Trusted Execution Technology Support
- PCI Express 3.0 Interface (up to 8 GT/s)
- SATA Controller (up to 6Gb/sec)
- Advanced Host Controller Interface (AHCI)

1-3 Motherboard Features

СРИ	Single Intel Core i3/i5/i7 6th generation processor in an LGA1151 type socket.		
Memory	Four (4) slots support up to 64GB of unbuffered, non-ECC, 3000+MHz(OC) DDR4 memory*		
	Dual-channel me	mory	
	DIMM sizes		
	UDIMM 40	GB, 8GB, 16GB	
Chipset	Intel® Z170 Exp	ress	
Expansion Slots	Two (2) PCH PCI-E 3.0 X1 (in X4) slot		
	One (1) CPU PCI-	-E 3.0 X4 (in X16) slot	
	One (1) PCH PCI	-E 3.0 X4 slot	
	One (1) CPU PCI-	-E 3.0 X16 slot	
	One (1) CPU PCI-E 3.0 X8 (in X16) slot		
	One (1) M.2 slot		
Network Connections	One (1) Gigabit Ethernet Controller		
	One (1) RJ-45 rear I/O panel connectors with Link and Activity LEDs		
I/O Devices	S	ATA Connections	
	SATA 3.0 (6Gb/s)	Six (6) I-SATA 0~5, via Intel Z170	
		RAID 0, 1, 5, 10	
		USB Devices	
	Two (2) USB 2.0, Two (2) USB 3.0, one (1) USB 3.1 'type C' ports on the rear I/O panel		
	Four (4) Front-Accessible USB 2.0 ports on two headers and Four (4) Front Accessible USB 3.0 ports on two headers.		
	Keyboard/Mouse		
	One shared PS/2 Keyboard/Mouse port on the I/O backpanel		
	Other I/O Ports		
	One (1) DisplayPort, One (1) DVI-D Port, One (1) HDMI Port		
	One (1) Serial Port header (COM1)		

	Audio	
	One (1) High Definition Audio 7.1 channel connector supported by Realtek ALC1150 on the back panel	
	One (1) Front Panel Audio Header	
	One (1) S/PDIF Out on the rear side of the chassis	
	Super I/O	
	Nuvoton NCT6792D-B	
BIOS	128 Mb AMI BIOS [®] SPI Flash BIOS	
	Plug and Play (PnP0, DMI 2.8, PCI 2.3, ACPI 1.0/2.0/3.0, and USB Keyboard	
Power Configuration	ACPI/ASPM Power Management	
	Main Switch Override Mechanism	
	Internal/External Modem Ring-On	
	Power-on mode for AC power recovery	
Health Monitoring	CPU Monitoring	
	Onboard monitors: CPU core, +3.3V, +5V, +/- 12V, +3.3V Stby, +5V Stby, VBAT, HT, Memory PCH Temperature, System Temperature, and CPU Temperature	
	CPU 6+1 phase switching voltage regulator	
	CPU/System overheat LED and control	
	CPU Thermal Trip support	
	Thermal Monitor support	
	Fan Control	
	Fan status monitoring with firmware 4-pin fan speed control	
	Low noise fan speed control	
System Management	PECI (Platform Environment Configuration Inter- face) 2.0 support	
	System resource alert via SuperDoctor ${}^{\textcircled{R}}$ III	
	SuperDoctor III, NMI	
	Chassis Intrusion header and detection	
CD Utilities	BIOS flash upgrade utility	
	Drivers and software for Intel® Z170 Express chipset utilities	
Other	ROHS 6/6 (Full Compliance, Lead Free)	
Dimensions	ATX form factor (12.0" x 9.6") (304.8 mm x 243.84 mm)	

1-4 Special Features

Recovery from AC Power Loss

Basic I/O System (BIOS) provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off, (in which case you must press the power switch to turn it back on), or for it to automatically return to a power-on state. See the Advanced BIOS Setup section to change this setting. The default setting is **Last State**.

1-5 PC Health Monitoring

This section describes the PC health monitoring features of the board. All have an onboard System Hardware Monitoring chip that supports PC health monitoring. An onboard voltage monitor will scan these onboard voltages continuously: CPU core, +3.3V, +5V, +/- 12V, +3.3V Stby, +5V Stby, VBAT, HT, Memory PCH Temperature, System Temperature, and CPU Temperature. Once a voltage becomes unstable, a warning is given, or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware Control

PC health monitoring in the BIOS can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by Thermal Management via SIO.

Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once the thermal sensor detects that the CPU temperature is too high, it will automatically turn on the thermal fans to prevent the CPU from overheating. The onboard chassis thermal circuitry can monitor the overall system temperature and alert the user when the chassis temperature is too high.

 \mathbb{N}

Note: To avoid possible system overheating, please be sure to provide adequate airflow to your system.

System Resource Alert

This feature is available when the system is used with SuperDoctor III in the Windows OS environment or used with SuperDoctor II in Linux. SuperDoctor is used to notify the user of certain system events. For example, you can also configure SuperDoctor to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond predefined thresholds.

1-6 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with Windows 7, Windows 8, and Windows 2008 Operating Systems.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start to blink to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will "wake up", and the LED will automatically stop blinking and remain on.

1-7 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

This motherboard accommodates 24-pin ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. In addition, the 12V 8-pin power connector located at JPW2 is also required to ensure adequate power supply to the system. Also your power supply must supply 1.5A for the Ethernet ports.

Attention! To prevent damage to the power supply or motherboard, please use a power supply that contains a 24-pin and a 8-pin power connectors. Be sure to connect these connectors to the 24-pin (JPW1) and the 8-pin (JPW2) power connectors on the motherboard.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant. (For more information, please refer to the web site at http://www.ssiforum.org/). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1-8 Super I/O

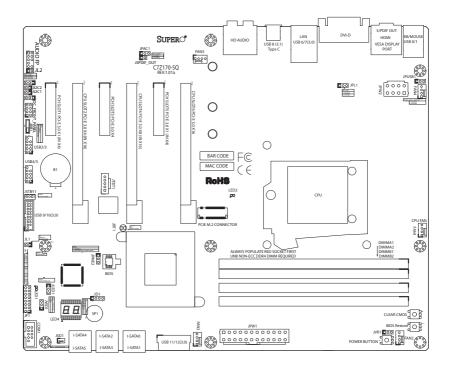
The Super I/O supports two high-speed, 16550 compatible serial communication ports (UARTs). Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.



C7Z170-SQ Motherboard Image

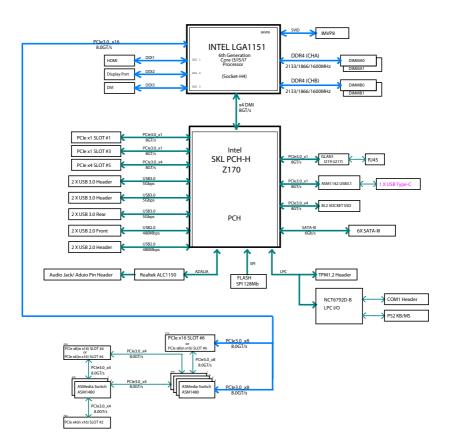
Note: All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.



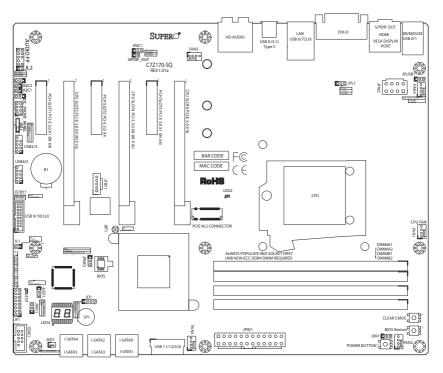
C7Z170-SQ Motherboard Layout

Important Notes to the User

- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "
 "
 indicates the location of "Pin 1".
- Jumpers not indicated are for testing only.
- When LED1 (Onboard Power LED Indicator) is on, system power is on. Unplug the power cable before installing or removing any components.



C7Z170-SQ Block Diagram



C7Z170-SQ Quick Reference

Jumper	Description	Default
JBT1	Clear CMOS (on board)	(See Chpt. 2)
JI ² C1/JI ² C2	SMB to PCI-E Slots	On (Enabled)
JPAC1	Audio Enable	Pins 1-2 (Enabled)
JPL1	LAN Enable	Pins 1-2 (Enabled)
JPME2	Intel Recovery Mode	Pins 1-2 (Normal)
JWD1	Watch Dog Enable	Pins 1-2 (RST)
JBR1	BIOS Recovery Switch	Toggle Switch, Off (Disabled)
JPUSB1	USB Wake Up Enable (Back Panel USB 0/1)	Pins 1-2 (Enabled)
POWER BUTTON	Internal Power Button	Push Button Switch
BIOS Restore	Restores the BIOS from Firmware ('SUPER.	Push Button Switch
	ROM') on a USB memory device	
Clear CMOS	Clear CMOS Switch	Push Button Switch

Connector	Description	
I/O Back Panel	See Back Panel I/O Connectors, below right	
Audio FP	Front Panel Audio Header	
Battery	Onboard Battery	
COM1	COM1 Port Header	
Fan 1,2,3,4,5	System/CPU Fan Headers (Fan1: CPU Fan)	
JD1	Speaker/buzzer (Pins 1~4: External Speaker, Pins 3~4: Buzzer)	
JF1	Front Panel Control Header	
JL1	Chassis Intrusion Header	
JLED1	Power LED Indicator Header	
JPW1	24-pin ATX Main Power Connector (Required)	
JPW2	+12V 8-pin CPU power Connector (Required)	
JSD1	SATA DOM (Disk On Module) Power Connector	
JSPDIF_OUT	Sony/Philips Digital Interface (S/PDIF) Out Header	
JSTBY1	5V Standby Power Header	
SP1	Internal Speaker/Buzzer	
I-SATA0~5	(Intel Z170) Serial ATA (SATA 3.0) Ports 0~5 (6Gb/sec)	
USB 2/3, 4/5	Front Panel Accessible USB 2.0 Headers 2/3, 4/5	
USB 9/10 (3.0)	Front Panel Accessible USB 3.0 Header 9/10	
USB 11/12 (3.0)	Front Panel Accessible USB 3.0 Header 11/12	
OC FRONT PANEL	Header for the Overclocking Control Panel	
PCI-E M.2 CONNECTOR	PCI-E M.2 Connector for memory cards, wireless adapters, etc.	

LED	Description	Color/State	Status
LED1	Onboard Standby PWR LED	Green: Solid on	Power On
LED2	M.2 LED	Green: M.2 on board	M.2 on board
LED4	Status Display	Digital Readout	Download the sta- tus codes below*

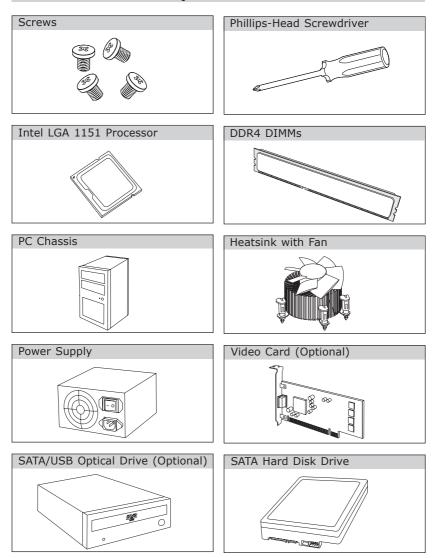
*Download the AMI status codes at http://www.ami.com/support/doc/ami_aptio_4.x_status_codes_pub.pdf

Notes

Chapter 2

Installation

2-1 Installation Components and Tools Needed



2-2 Static-Sensitive Devices

Electrostatic-Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery. Do not install the onboard battery upside down to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure that the person handling it is static protected.

2-3 Processor and Heatsink Installation

Attention! When handling the processor package, avoid placing direct pressure on the label area of the fan.



Important:

Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.

If you buy a CPU separately, make sure that you use an Intelcertified multi-directional heatsink only.

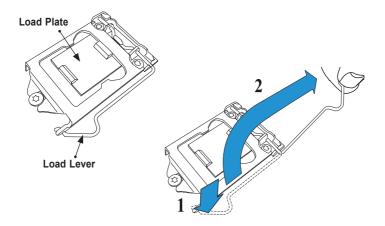
Make sure to install the system board into the chassis before you install the CPU heatsink.

When receiving a server board without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.

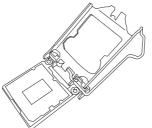
Refer to the Supermicro website for updates on CPU support.

Installing the LGA1151 Processor

1. Press the load lever to release the load plate, which covers the CPU socket, from its locking position.



2. Gently lift the load lever to open the load plate. Remove the plastic cap.

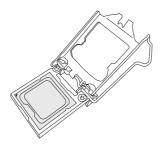


3. Use your thumb and your index finger to hold the CPU at the North center edge and the South center edge of the CPU.

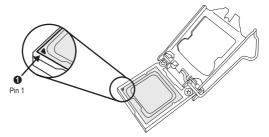


South Center Edge

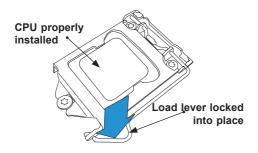
 Align the CPU key that is the semi-circle cutouts against the socket keys. Once it is aligned, carefully lower the CPU straight down into the socket. (Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically.



5. Do not rub the CPU against the surface or against any pins of the socket to avoid damaging the CPU or the socket.)



- 6. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.
- 7. Use your thumb to gently push the load lever down to the lever lock.



Attention! You can only install the CPU inside the socket only in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the load plate again and double-check that the CPU is aligned properly.

Installing an Active CPU Heatsink with Fan

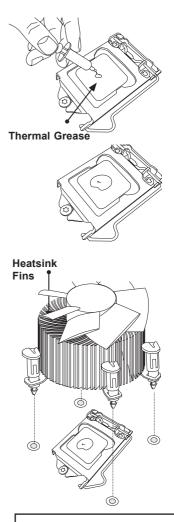
- Locate the CPU Fan power connector on the motherboard. (Refer to the layout on the right for the CPU Fan location.)
- Position the heatsink so that the heatsink fan wires are closest to the CPU fan power connector and are not interfered with other components.
- Inspect the CPU Fan wires to make sure that the wires are routed through the bottom of the heatsink.
- Remove the thin layer of the protective film from the heatsink.

Attention! CPU overheating mayoccur if the protective film is not removed from the heatsink.

5. Apply the proper amount of thermal grease on the CPU.

Note: if your heatsink came with a thermal pad, please ignore this step.

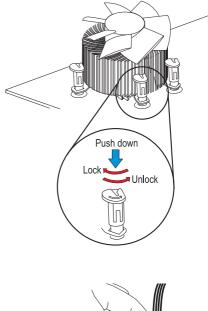
 If necessary, rearrange the wires to make sure that the wires are not pinched between the heatsink and the CPU. Also make sure to keep clearance



Recommended Supermicro heatsink: SNK-P0046A4 active heatsink

between the fan wires and the fins of the heatsink.

- Align the four heatsink fasteners with the mounting holes on the motherboard. Gently push the pairs of diagonal fasteners (#1 & #2, and #3 & #4) into the mounting holes <u>until you</u> <u>hear a click</u>. Also, make sure to orient each fastener so that the narrow end of the groove is pointing outward.
- Repeat Step 7 to insert all four heatsink fasteners into the mounting holes.
- Once all four fasteners are securely inserted into the mounting holes, and the heatsink is properly installed on the motherboard, connect the heatsink fan wires to the CPU Fan connector.



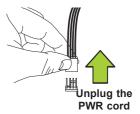


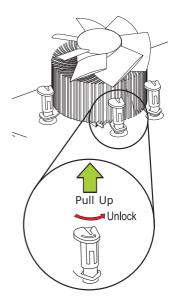
Removing the Heatsink

Attention! We do not recommend that the CPU or the heatsink be removed. However, if you do need to remove the heatsink, please follow the instructions below to remove the heatsink and to prevent damage done to the CPU or other components.

Active Heatsink Removal

- 1. Unplug the power cord from the power supply.
- 2. Disconnect the heatsink fan wires from the CPU fan header.
- Use your finger tips to gently press on the fastener cap and turn it counterclockwise to make a 1/4 (90°) turn, and pull the fastener upward to loosen it.
- 4. Repeat Step 3 to loosen all fasteners from the mounting holes.
- With all fasteners loosened, remove the heatsink from the CPU.





2-4 Installing DDR4 Memory



Note: Check the Supermicro website for recommended memory modules.

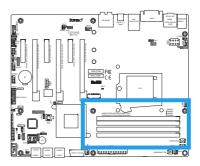
Attention! Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

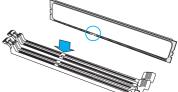
DIMM Installation

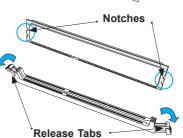
- Insert the desired number of DIMMs into the memory slots, starting with DIMMA1 (see the next page for the location). For the system to work properly, please use the memory modules of the same type and speed in the same motherboard.
- Push the release tabs outwards on both ends of the DIMM slot to unlock it.
- Align the key of the DIMM module with the receptive point on the memory slot.
- Align the notches on both ends of the module against the receptive points on the ends of the slot.
- Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.
- Press the release tabs to the lock positions to secure the DIMM module into the slot.

Removing Memory Modules

Reverse the steps above to remove the DIMM modules from the motherboard.

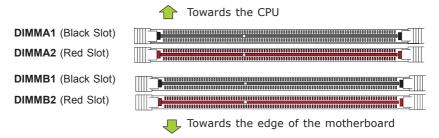






Press both notches straight down into the memory slot.

Memory Support



The C7Z170-SQ supports up to 64GB of Unbuffered (UDIMM) non-ECC DDR4 memory, up to 3000+MHz (OC) in four 288-pin memory slots. Populating these DIMM modules with a pair of memory modules of the same type and same size will result in interleaved memory, which will improve memory performance.

Notes

Be sure to use memory modules of the same type, same speed, same frequency on the same motherboard. Mixing of memory modules of different types and speeds is not allowed.

Due to memory allocation to system devices, the amount of memory that remains available for operational use will be reduced when 4 GB of RAM is used. The reduction in memory availability is disproportional. See the following table for details.

For Microsoft Windows users: Microsoft implemented a design change in the Windows XP with Service Pack 2 (SP2) and Windows Vista. This change is specific to the behavior of Physical Address Extension (PAE) mode which improves driver compatibility. For more information, please read the following article at

Microsoft's Knowledge Base website at: http://support.microsoft. com/kb/888137.

Possible System Memory Allocation & Availability			
System Device		Physical Memory Remaining (-Available) (4 GB Total System Memory)	
Firmware Hub flash memory (System BIOS)	1 MB	3.99	
Local APIC	4 KB	3.99	
Area Reserved for the chipset	2 MB	3.99	
I/O APIC (4 Kbytes)	4 KB	3.99	
PCI Enumeration Area 1	256 MB	3.76	
PCI Express (256 MB)	256 MB	3.51	
PCI Enumeration Area 2 (if needed) -Aligned on 256-MB boundary-	512 MB	3.01	
VGA Memory	16 MB	2.85	
TSEG	1 MB	2.84	
Memory available to OS and other applications		2.84	

Memory Population Guidelines

When installing memory modules, the DIMM slots should be populated in the following order: DIMMA2, DIMMB2, then DIMMA1, DIMMB1.

- Always use DDR4 DIMM modules of the same size, type and speed.
- Mixed DIMM speeds can be installed. However, all DIMMs will run at the speed of the slowest DIMM.

Recommended Population (Balanced)				
DIMMA2 DIMMB2 DIMMA1 DIMMB1 Total System Merr			Total System Memory	
4GB	4GB			8GB
4GB	4GB	4GB	4GB	16GB
8GB	8GB			16GB
8GB	8GB	8GB	8GB	32GB
16GB	16GB			32GB
16GB	16GB	16GB	16GB	64GB

2-5 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.



Philips Screwdriver

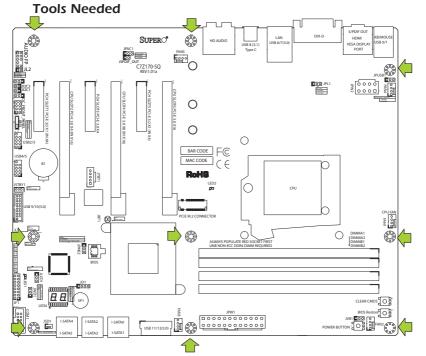
(1)



Philips Screws (9)



Standoffs (9) Only if Needed



Location of Mounting Holes

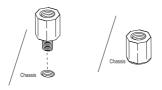
Attention! 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

Installing the Motherboard

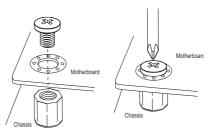
1. Install the I/O shield into the back of the chassis.



- 2. Locate the mounting holes on the motherboard. (See the previous page.)
- 3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.



- 4. Install standoffs in the chassis as needed.
- 5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.

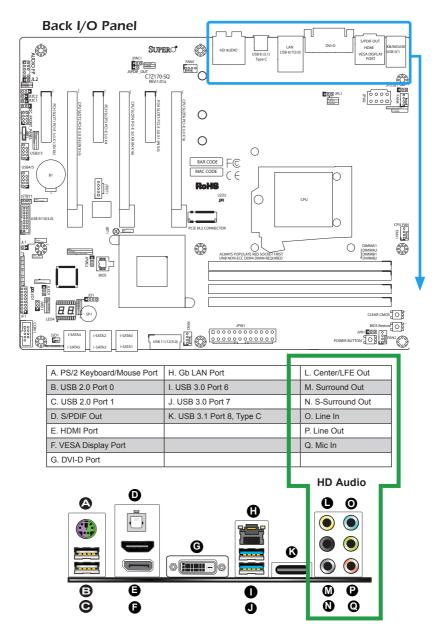


- Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
- 7. Repeat Step 5 to insert #6 screws into all mounting holes.
- 8. Make sure that the motherboard is securely placed in the chassis.

Note: Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

2-6 Connectors/IO Ports

The I/O ports are color coded in conformance with the industry standards. See the figure below for the colors and locations of the various I/O ports.



Universal Serial Bus (USB)

Two Universal Serial Bus 2.0 ports (#0/1), two USB 3.0 ports (#6/7) and one USB 3.1 'type C' port (#8) are located on the I/O back panel. In addition, two USB 3.0 headers (four ports: #9/10, #11/12), and one USB 2.0 header (two ports: #4/5) are also located on the motherboard to provide front chassis access using USB cables (not included). See the tables below for pin definitions.

Front Panel USB (2.0) Header #4/5 Pin Definitions			
Pin # Definition Pin # Definition			
1	+5V	2	+5V
3	USB_PN2	4	USB_PN3
5	USB_PP2	6	USB_PP3
7	Ground	8	Ground
9	Key	10	Ground

Signal Name

VBUS

D-

D+

Ground

StdA SSRX-

StdA SSRX+

GND DRAIN

StdA SSTX-

StdA SSTX+

Pin#

1

2

3

4

5

6

7

8

9

Pin#

10

11

12

13

14

15

16

17

18

Front Panel USB (3.0) Header #9/10, #11/12

Pin Definitions

Description

USB 2.0 Differential Pair

Ground of PWR Return

SuperSpeed Receiver

Ground for Signal Return

SuperSpeed Transmitter

Differential Pair

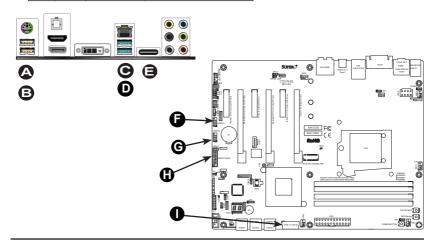
Differential Pair

Power

Back Panel USB (2.0) #0/1, USB (3.0) #6/7 Pin Definitions				
Pin#	Pin# Definition Pin# Definition			
1	+5V 5 +5V			
2	USB_PN1 6 USB_PN0			
3	USB_PP1	7	U	ISB_PP0
4	Ground	8	G	Fround

Α.	Backpanel	USB	2.0	#0

- B. Backpanel USB 2.0 #1
- C. Backpanel USB 3.0 #6
- D. Backpanel USB 3.0 #7
- E. Backpanel USB 3.1 #8
- F. USB 2.0 Header #2/3
- G. USB 2.0 Header #4/5
- H. USB 3.0 Header #9/10
- I. USB 3.0 Header #11/12



Ethernet Port

One Gigabit Ethernet port (LAN) is located next to the DVI-D port on the I/O Backpanel to provide network connections. This port will accept RJ45 type cables.



Note: Please refer to the LED Indicator Section for LAN LED information.

LAN Ports Pin Definition			
Pin#	Definition		
1	P2V5SB	10	SGND
2	TD0+	11	Act LED
3	TD0-	12	P3V3SB
4	TD1+	13	Link 100 LED (Green, +3V3SB)
5	TD1-	14	Link 1000 LED (Yellow, +3V3SB)
6	TD2+	15	Ground
7	TD2-	16	Ground
8	TD3+	17	Ground
9	TD3-	18	Ground

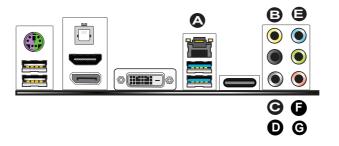
(NC: No Connection)

Back Panel High Definition Audio (HD Audio)

This motherboard features a 7.1+2 Channel High Definition Audio (HDA) codec that provides 10 DAC channels. The HD Audio connections simultaneously supports multiple-streaming 7.1 sound playback with 2 channels of independent stereo output through the front panel stereo out for front, rear, center and subwoofer speakers. Use the Advanced software included in the CD-ROM with your motherboard to enable this function.

A. LAN1

- B. Center/LFE Out
- C. Surround Out
- D. S-Surround
- E. Line In
- F. Line Out
- G. Mic In



S/PDIF Port

A S/PDIF port is located next to the USB ports 0/1 on the I/O backpanel. Use this port to connect to a compatible S/PDIF optical audio device.

VESA[®] **DisplayPort**[™]

DisplayPort, develped by the VESA consortium, delivers digital display at a fast refresh rate. It can connect to virtually any display device using a DisplayPort adapter for devices such as VGA, DVI or HDMI.

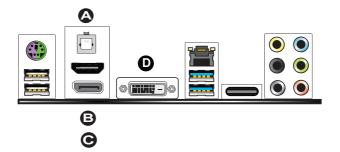
HDMI Port

One HDMI (High-Definition Multimedia Interface) is located on the I/O backpanel. This connector is used to display both high definition video and digital sound through an HDMI capable display, using a single HDMI cable (not included).

DVI-D Port

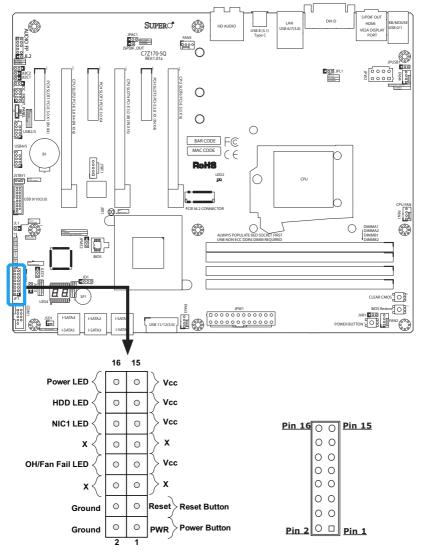
A DVI-D port is located on the I/O backpanel. Use this port to connect to a compatible DVI (Digital Visual Interface) display.

A. S/PDIF PortB. HDMI PortC. VESA Display PortD. DVI-D



Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the descriptions of the front control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.



JF1 Header Pins

Front Control Panel Pin Definitions

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable here to indicate the status of HDDrelated activities, including IDE, SATA activities. See the table on the right for pin definitions.

NIC1 (LAN)

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1. Attach an LED indicator to this header to display network activity. Refer to the table on the right for pin definitions.

Overheat (OH)/Fan Fail

Connect an LED cable to OH/Fan Fail connections on pins 7 and 8 of JF1 to provide warnings for chassis overheat/ fan failure. Refer to the table on the right for pin definitions.

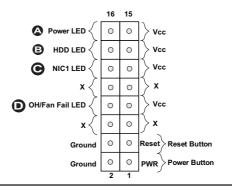
Power LED Pin Definitions (JF1)		
Pin#	Definition	
15	+5V	
16 Ground		

HDD LED Pin Definitions (JF1)		
Pin#	Definition	
13	+5V	
14 HD Active		

LAN LED Pin Definitions (JF1)		
Pin# Definition		
9/11 Vcc		
10/12 Ground		

OH/Fan Fail LED Pin Definitions (JF1)		
Pin# Definition		
7 Vcc/Blue UID LED		
8 OH/Fan Fail LED		

OH/Fan Fail Indicator Status		
State Definition		
Off	Normal	
On	Overheat	
Flash- ing	Fan Fail	





Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. Refer to the table on the right for pin definitions.

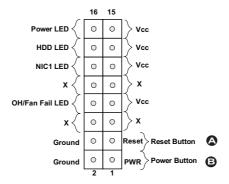
Power Button

The Power Button connection is located on pins1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power in the suspend mode, press the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)		
Pin#	Definition	
3	Reset	
4 Ground		

Power Button Pin Definitions (JF1)		
Pin#	Definition	
1	Signal	
2 Ground		

A. Reset Button B. PWR Button



2-7 Connecting Cables

This section provides brief descriptions and pin-out definitions for onboard headers and connectors. Be sure to use the correct cable for each header or connector.

ATX Main PWR & CPU PWR Connectors (JPW1 & JPW2)

The 24-pin main power connector (JPW1) is used to provide power to the motherboard. The 8-pin CPU PWR connector (JPW2) is also required for the processor. These power connectors meet the SSI EPS 12V specification. See the table on the right for pin definitions.

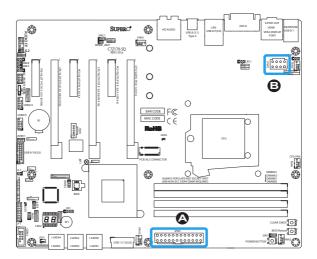
12V 8-pin Power Connec- tor Pin Definitions		
Pins Definition		
1 through 4	Ground	
5 through 8 +12V		

(Required)

ATX Power 24-pin Connector Pin Definitions (JPW1)			
Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	СОМ	12	+3.3V

A. 24-Pin ATX Main PWR

B. 8-Pin PWR



Fan Headers (Fan 1 ~ Fan 5)

The C7Z170-SQ has five fan headers (Fan 1~Fan 5). These fans are 4-pin fan headers. Although pins 1-3 of the fan headers are backward compatible with the traditional 3-pin fans, we recommend the use 4-pin fans to take advantage of the fan speed control. This allows the fan speeds to be automatically adjusted based on the motherboard temperature. Refer to the table on the right for pin definitions.

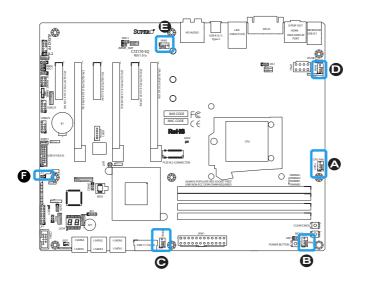
Chassis Intrusion (JL1)

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened.

Fan Header Pin Definitions		
Pin# Definition		
1	Ground (Black)	
2	2.5A/+12V (Red)	
3	Tachometer	
4	4 PWM_Control	

Chassis Intrusion Pin Definitions (JL1)		
Pin#	Definition	
1	Intrusion Input	
2	Ground	

<u>A. Fan 1 (CPU Fan)</u>
<u>B. Fan 2</u>
<u>C. Fan 3</u>
<u>D. Fan 4</u>
<u>E. Fan 5</u>
F. Chassis Intrusion



Internal Buzzer (SP1)

The Internal Buzzer (SP1) can be used to provide audible indications for various beep codes. See the table on the right for pin definitions.

Speaker (JD1)

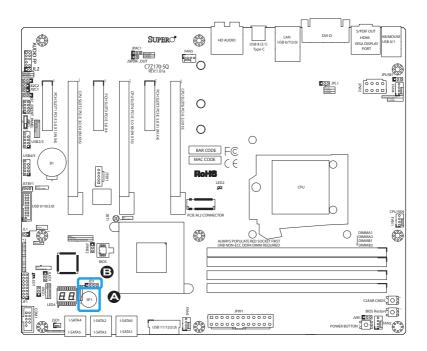
On the JD1 header, Pins $3 \sim 4$ are used for internal speaker. Close Pins $3 \sim 4$ with a cap to use the onboard speaker. If you wish to use an external speaker, close Pins $1 \sim 4$ with a cable. See the table on the right for pin definitions.

Internal Buzzer Pin Definition			
Pin#	Definitions		
Pin 1	Pos. (+)	Beep In	
Pin 2	Neg. (-)	Alarm Speaker	

Speaker Connector Pin Definitions		
Pin Setting	Definition	
Pins 3~4	Internal Speaker	
Pins1~4 External Speaker		

A. Internal Buzzer

B. Speaker Header



Onboard Power LED (JLED1)

An onboard Power LED header is located at JLED1. This Power LED header is connected to Front Control Panel located at JF1 to indicate the status of system power. See the table on the right for pin definitions.

Serial Port (COM1)

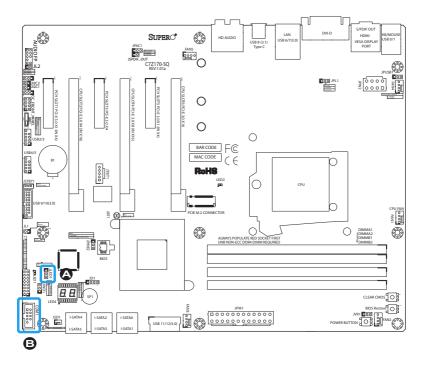
There is one serial (COM) port header on the motherboard. COM1 is located close to the SATA connectors. See the table on the right for pin definitions.

Onboard PWR LED Pin Definitions		
Pin#	Definition	
1	VCC	
2	No Connection	
3 Connection to PWR LED in JF1		

Serial/COM Ports Pin Definitions				
Pin #	Pin # Definition Pin # Definition			
1	DCD	6	DSR	
2	RXD	7	RTS	
3	TXD	8	CTS	
4	DTR	9	RI	
5	Ground	10	N/A	

A. PWR LED

B. COM1



The Disk-On-Module (DOM) power connector, located at JSD1, provides 5V (Gen1/Gen) power to a solid state DOM storage device connected to one of the SATA ports. See the table on the right for pin definitions.

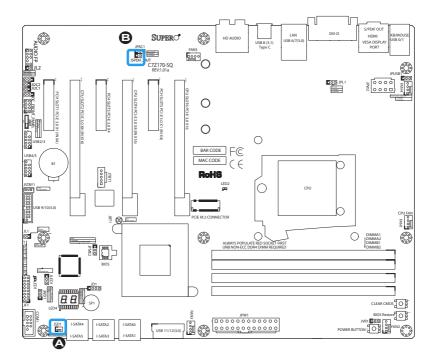
The SPDIF Out (JSPDIF_OUT) is used for digital audio output. You will also need the appropriate cable to use this feature.

DOM PWR Pin Definitions	
Pin#	Definition
1	5V
2	Ground
3	Ground

SPDIF_OUT Pin Definitions	
Pin#	Definition
1	S/PDIF_Out
2	Ground

A.DOM PWR

B. S/PDIF OUt



Standby Power Header (STBY1)

The Standby Power header is located at JSTBY1 on the motherboard. See the table on the right for pin definitions.

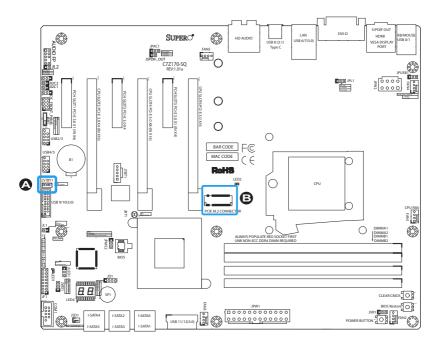
	Standby Power Pin Definitions	
Pin#	Definition	
1	+5V Standby	
2	Ground	
3	Wake-up	

PCI-E M.2 Connector (PCI-E M.2)

The PCI-E M.2 connector is for devices such as memory cards, wireless adapters, etc. These devices must conform to the PCIE M.2 specifications (fromerly known as NGFF). This particular PCIe M.2 supports M-Key (PCIe3.0 x4) storage card only.

A. STBY PWR

B. PCI-E M.2 Connector



Front Panel Audio Header (AUDIO FP)

A 10-pin Audio header is supported on the motherboard. This header allows you to connect the motherboard to a front panel audio control panet, if needed. Connect an audio cable to the audio header to use this feature (not supplied). See the table at right for pin definitions for the header.

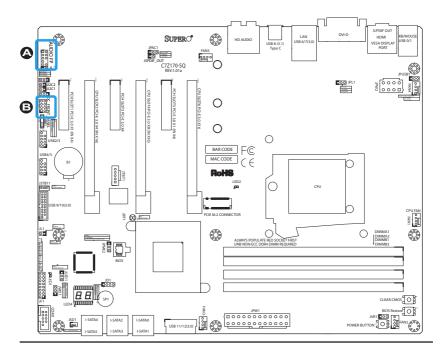
OC Front Panel (OC FRONT PANEL)

This header is for the optional Overlocking (OC) control panel. Attach the control panel's cable to this header. The OC control panel enables over-clocking control and management from the front of the chassis. See Section 2-10 for more details.

10-in Audio Pin Definitions	
Pin#	Signal
1	Microphone_Left
2	Audio_Ground
3	Microphone_Right
4	Audio_Detect
5	Line_2_Right
6	Ground
7	Jack_Detect
8	Key
9	Line_2_Left
10	Ground

<u>A. AUDIO FP</u>

B. OC FRONT PANEL

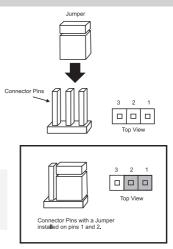


2-8 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board.

Note: On two pin jumpers, "Closed" means the jumper is on, and "Open" means the jumper is off the pins.

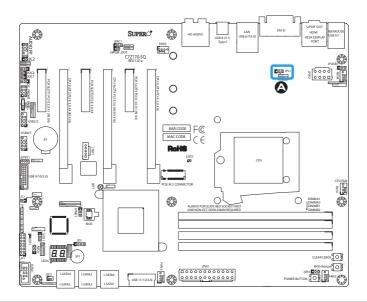


LAN Enable/Disable

Jumper JPL1 will enable or disable the LAN port on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

GLAN Enable Jumper Settings	
Pin#	Definition
1-2	Enabled (default)
2-3	Disabled

A. JPL1: LAN Enable



Clear CMOS & JBT1

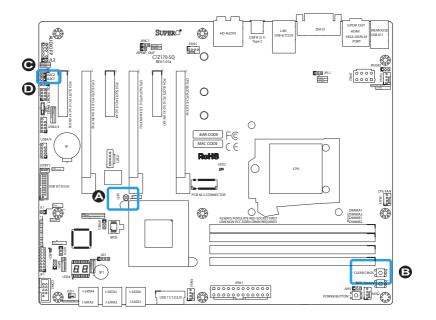
Clear CMOS and JBT1 are used to clear the saved system setup configuration stored in the CMOS chip. To clear the contents of the CMOS usng JBT1, short the two pads of JBT1 with metallic conductor such as a flathead screwdriver. Clear CMOS works the same way but is a push button switch. This will erase all user settings and revert everything to their factory-set defaults.

PCI Slot SMB Enable (I²C1/I²C2)

Use Jumpers I^2C1/I^2C2 to enable PCI SMB (System Management Bus) support to improve system management for the PCI slots. See the table on the right for jumper settings.

PCI Slot_SMB Enable Jumper Settings		
Jumper Setting	De	finition
Short (Default)		Enabled
Open		Disabled





Audio Enable (JPAC1)

JPAC1 allows you to enable or disable the onboard audio support. The default position is on pins 1 and 2 to enable onboard audio connections. See the table on the right for jumper settings.

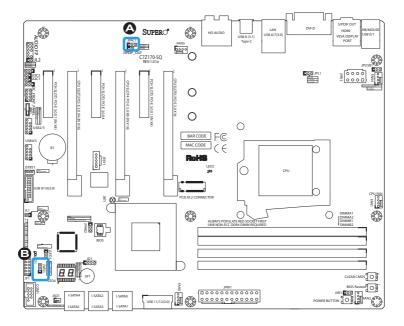
Watch Dog Enable/Disable

Watch Dog (JWD1) is a system monitor that can reboot the system when a software application hangs. Close Pins 1-2 to reset the system if an application hangs. Close Pins 2-3 to generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings.

Audio Enable/Disable Jumper Settings	
Both Jumpers	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

Watch Dog Jumper Settings		
Jumper Setting Definition		
Pins 1-2	Reset (default)	
Pins 2-3	NMI	
Open	Disabled	

A. Audio Enable B. Watch Dog Enable



USB Wake Up (JPUSB1)

Use jumper JPUSB to activate the "wakeup" function of the USB ports by pressing a key on a USB keyboard or clicking the USB mouse connected. This jumper is used together with a USB Wake-Up feature in the BIOS. Enable this jumper and the USB support in the BIOS to wake up your system via USB devices.

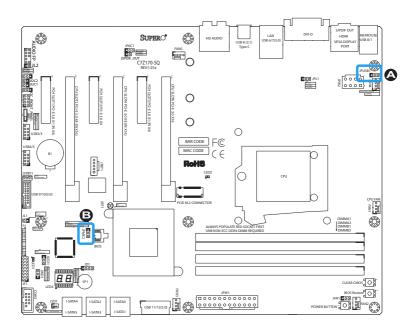
Manufacturing	Mode	(JPME2)
---------------	------	---------

Close Pin 2 and Pin 3 of Jumper JPME2 to bypass SPI flash security and force the system to operate in Manufacturing Mode, allowing the user to flash the system firmware from a host server for system setting modifications. See the table on the right for jumper settings.

USB Wake-Up Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled (Default)
Pins 2-3	Disabled

Manufacture Mode (JPME2) Jumper Settings		
Pin#	Definition	
1-2	Normal (Default)	
2-3	Manufacture Mode	

A. USB Wake Up B. Manufacturing Mode



BIOS Recovery Switch (JBR1)

The BIOS Recovery Switch (JBR1) is used to enable or disable the BIOS Recovery feature of the motherboard. Slide the switch from the default position to begin the recovery process. See Appendix D for details.

Power Button (POWER BUTTON)

In addition to the soft power switch provided in JF1, your motherboard is equipped with a 'soft' power button on the motherboard. This switch works the same way as the soft power switch on JF1.

BIOS Restore (BIOS RESTORE)

When pressed, the BIOS Restore Button will look for, and load a file named 'SU-PER.ROM' from an installed USB memory device, in any of the USB ports. It will then proceed to update the BIOS. Do NOT turn off the system when BIOS is updating.

BIOS Recovery (JBR1) Jumper Settings	
State	Definition
Off	Normal (Default)
On	Recover

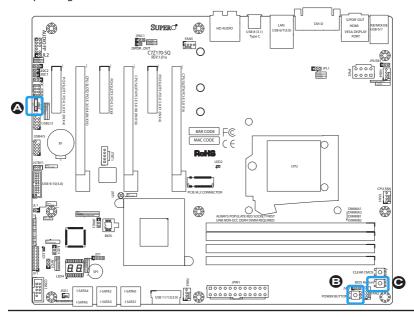


Manufacture Mode (JPME2) Jumper Settings		
Pin#	Definition	
1-2	Normal (Default)	
2-3	Manufacture Mode	

A. BIOS Recovery

B. Power Button

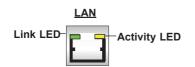
C. BIOS Restore



2-9 Onboard Indicators

LAN LEDs

One LAN port is located on the I/O backpanel of the motherboard. This Ethernet LAN port has two LEDs (Light Emitting Diode). The yellow LED indicates activity, while the Link LED may be green, amber, or off to indicate the speed of the connections. See the tables at right for more information.



Onboard Power LED (LED1)

An Onboard Power LED is located at LED1 on the motherboard. When LED1 is on, the AC power cable is connected. Make sure to disconnect the power cable before removing or installing any component. See the layout below for the LED location.

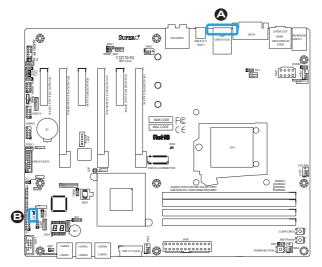
GLAN Activity Indicator LED Settings		
Color	Status	Definition
Yellow	Flashing	Active

GLAN Link Indicator LED Settings		
LED Color	Definition	
Off	No Connection/10 Mbps/100 Mbps	
Amber	1 Gbps	
Green	10 Gbps.	

Onboard PWR LED Indicator LED Status		
Status	Definition	
Off	System Off	
On	System on, or System off and PWR Cable Connected	

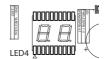
A. LAN LEDs

B. PWR LED



Status Display (LED4)

LED4 is made up of two alphanumeric displays that will display a status or POST code, when the motherboard is powered on. Please download the following AMI publication for a complete list of POST codes:



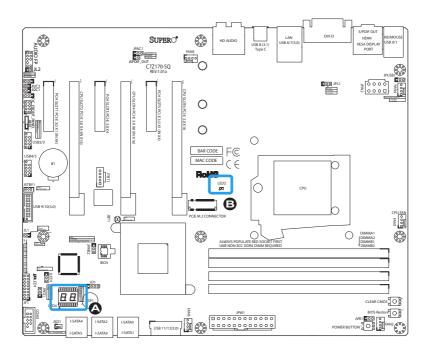
http://www.ami.com/support/doc/ami_aptio_4.x_status_codes_pub.pdf

M.2 On Board (LED2)

The M.2 On Board LED is located on LED2. When lit, this indicates that an M.2 device is detected in the M.2 slot of the motherboard, and is working normally.

A. Status Display

B. M.2 On Board



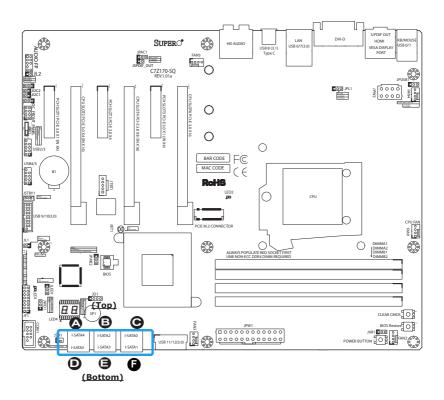
2-10 SATA Connections

SATA Connections (I-SATA0~I-SATA5)

Six Serial ATA (SATA) 3.0 connectors (I-SATA $0\sim5$) are supported on the board. These I-SATA 3.0 ports are supported by the Intel Z170 PCH chip (supports RAID 0,1,5,10). See the table below for pin definitions.

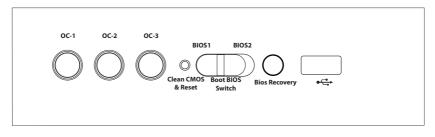
SATA 2.0/3.0 Connectors Pin Definitions		
Pin#	Signal	
1	Ground	
2	SATA_TXP	
3	SATA_TXN	
4	Ground	
5	SATA_RXN	
6	SATA_RXP	
7	Ground	

Top A. I-SATA 3.0 #4 B. I-SATA 3.0 #2 C. I-SATA 3.0 #0 Bottom D. I-SATA 3.0 #5 E. I-SATA 3.0 #3 F. I-SATA 3.0 #1



2-11 The OC Front Control Panel (Optional)

The OC (Overclocking) Front Control Panel has Six (6) control buttons, switches and one USB port.



Overclocking Buttons (OC1, OC2, OC3)

Press these buttons to activate the overclocking feature of the motherboard. These buttons will allow the CPU to run above its rated speed. This is an advanced feature and should only be used by experienced users. These work the same way as the OC buttons on the motherboard, see Page 2-32 for details.

Clear CMOS and Reset

Use the push-button hardware switch to clear the CMOS memory back to its default values and reset the system. There is also a similar switch on the motherboard (JBT1), but is made up of two contact pads that need to be shorted with a metallic object (i.e., screwdriver, etc).

Boot BIOS Switch

The Boot BIOS Switch is used to select between two boot BIOS blocks that are installed on the motherboard. See Appendix D for details.

BIOS Recovery

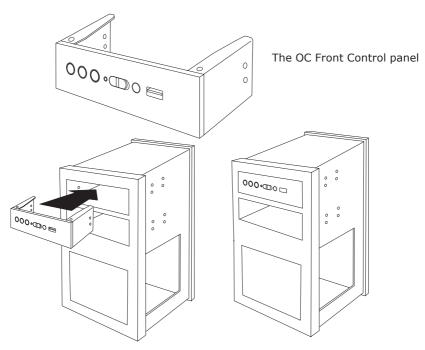
Push the BIOS Recovery button to enable the motherboard to enter the BIOS Recovery process and re-install the BIOS, reverting all settings back to factory default. It looks for a file named "SUPER.ROM" from a memory device attached to any of the USB drives. It functions the same way as the BIOS Restore Button which is installed on the motherboard, see page 2-34.

Front USB Port

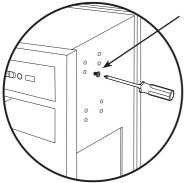
The Front USB Port is defined as USB 3.0 Port# XX on this motherboard. It is conveniently located on the OC panel for quick access for USB memory sticks and other storage devices.

Installing the OC Front Control Panel

The OC Front Control Panel is designed to fit into an external 5.25-inch external drive bay of a desktop-style or tower-style computer chassis. Make sure that the appropriate type of screws are used.



The OC Control panel easily slides into a standard 5.25-inch drive bay on a desktop or tower-style computer chassis.



Make sure to secure the OC Control Panel with the correct screws, but do not over-tighten.

Connect all the OC Control Panel cables to the appropriate headers located on the motherboard.



Notes

Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any hardware components.

Before Power On

- Make sure that the Standby PWR LED is not on. (Note: If it is on, the onboard power is on. Be sure to unplug the power cable before installing or removing the components.)
- 2. Make sure that there are no short circuits between the motherboard and chassis.
- 3. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse. Also, be sure to remove all add-on cards.
- Install a CPU and heatsink (-be sure that it is fully seated) and then connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.

No Power

- 1. Make sure that there are no short circuits between the motherboard and chassis.
- 2. Make sure that all jumpers are set to their default positions.
- 3. Check if the 115V/230V switch on the power supply is properly set.
- 4. Turn the power switch on and off to test the system.
- The battery on your motherboard may be old. Check to make sure that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

- 1. If the power is on, but you have no video--in this case, you will need to remove all the add-on cards and cables first.
- 2. Use the speaker to determine if any beep codes exist. (Refer to Appendix A for details on beep codes.)
- Remove all memory modules and turn on the system. (If the alarm is on, check the specifications of memory modules, reset the memory or try a different one.)

Memory Errors

- 1. Make sure that the DIMM modules are properly installed and fully seated in the slots.
- You should be using unbuffered Non-ECC DDR4 (up to 3000) MHz memory recommended by the manufacturer. Also, it is recommended that you use the memory modules of the same type and speed for all DIMMs in the system. Do not use memory modules of different sizes, different speeds and different types on the same motherboard.
- Check for bad DIMM modules or slots by swapping modules between slots to see if you can locate the faulty ones.
- 4. Check the switch of 115V/230V power supply.

When the System is Losing the Setup Configuration

- Please be sure to use a high quality power supply. A poor quality power supply may cause the system to lose CMOS setup information. Refer to Section 1-5 for details on recommended power supplies.
- The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
- 3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2 Technical Support Procedures

Before contacting Technical Support, please make sure that you have followed all the steps listed below. Also, Note that as a motherboard manufacturer, Supermicro does not sell directly to end users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

- Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our website (<u>http://www.supermicro.com/support/faqs/</u>) before contacting Technical Support.
- BIOS upgrades can be downloaded from our website at (<u>http://www.supermicro.com/support/bios/</u>).



Note: Not all BIOS can be flashed. Some cannot be flashed; it depends on the boot block code of the BIOS.

- If you've followed the instructions above to troubleshoot your system, and still cannot resolve the problem, then contact Supermicro's technical support and provide them with the following information:
- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration
- An example of a Technical Support form is on our website at (http://www.su-permicro.com/support/contact.cfm).
- Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.

3-3 Frequently Asked Questions

Question: What type of memory does my motherboard support?

Answer: The C7ZZ170-SQ supports up to 64GB of unbuffered Non-ECC DDR4. See Section 2-4 for details on installing memory.

Question: How do I update my BIOS?

Answer: We do NOT recommend that you upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <u>http://www.supermicro.com/support/bios/</u>. Please check our BIOS warning message and the information on how to update your BIOS on our web site. Select your motherboard model and download the BIOS ROM file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You may choose the zip file or the .exe file. If you choose the zipped BIOS file, please unzip the BIOS file onto a bootable device or a USB pen/thumb drive. To flash the BIOS, run the batch file named "ami.bat" with the new BIOS ROM file from your bootable device or USB pen/thumb drive. Use the following format:

F:\> ami.bat BIOS-ROM-filename.xxx <Enter>

Note: Always use the file named "ami.bat" to update the BIOS, and insert a space between "ami.bat" and the filename. The BIOS-ROM-filename will bear the motherboard name (i.e., C7Z170) and build version as the extension. For example, "C7Z170.115". When completed, your system will automatically reboot.

If you choose the .exe file, please run the .exe file under Windows to create the BIOS flash floppy disk. Insert the floppy disk into the system you wish to flash the BIOS. Then, boot the system to the floppy disk. The BIOS utility will automatically flash the BIOS without any prompts. Please note that this process may take a few minutes to complete. Do not be concerned if the screen is paused for a few minutes.

When the BIOS flashing screen is completed, the system will reboot and will show "Press F1 or F2". At this point, you will need to load the BIOS defaults. Press <F1> to go to the BIOS setup screen, and press <F9> to load the default settings. Next, press <F10> to save and exit. The system will then reboot.

Attention! Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!

Question: I think my BIOS is corrupted. How can I recover my BIOS? **Answer:** Please see Appendix C-BIOS Recovery for detailed instructions.

3-4 Battery Removal and Installation

Battery Removal

To remove the onboard battery, follow the steps below:

- 1. Power off your system and unplug your power cable.
- 2. Locate the onboard battery as shown below.
- 3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
- 4. Remove the battery.

Proper Battery Disposal



Attention! Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Battery Installation

- 1. To install an onboard battery, follow the steps 1& 2 above and continue below:
- 2. Identify the battery's polarity. The positive (+) side should be facing up.
- 3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

Attention! When replacing a battery, be sure to only replace it with the same type.



3-5 Returning Motherboard for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. For faster service, you may also obtain RMA authorizations online (http://www.supermicro.com/support/rma/). When you return the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS

4-1 Introduction

This chapter describes the AMI BIOS Setup Utility for the C7Z170-SQ. The ROM BIOS is stored in a Flash EEPROM and can be easily updated. This chapter describes the basic navigation of the AMI BIOS Setup Utility setup screens.



Note: For AMI BIOS Recovery, please refer to the UEFI BIOS Recovery Instructions in Appendix C.

Starting BIOS GUI Setup Utility

To enter the AMI BIOS GUI Setup Utility screens, press the <Delete> key while the system is booting up.



Note: In most cases, the <Delete> key is used to invoke the AMI BIOS setup screen.

SYSTEM INFORMATION	Motherboard CPU	Memory	
PROCESSOR/ CPU	Supermicro C72170-SQ BIOS Version		
III NEMORY			
+ BOOTING			
DIFUT/OUTPUT			
RE HAVE NONETOR			
E CHIPSET		<u> </u>	
SECURITY	Profiles	SUPERMICR	Save & Lo

Each BIOS menu option is described in this manual. The Main BIOS Setup screen has two main areas. The left area is the Main Navigation, and the main area is for the Setup Section. Icons that do not respond when the mouse pointer is hovering on top are not configurable.

Prefetcher	Enabled (V)	
Cache Line Prefetch	Enabled (V)	
	Enabled (V)	
ormance Mode	Turbo Performance	
	Power Saving Max Non-Turbo Performance Turbo Performance	

The AMI BIOS GUI Setup Utility uses a mouse pointer navigation system similar to standard graphical user interfaces. Hover and click an icon to select a section, click a down arrow to select from an options list.

\equiv	DIMM#A2 DIMM#B1 DIMM#B2	8192 MB 8192 MB 8192 MB	
TPUT		General Help Arows : Move Between Options Enter : Select +/- : Value ESC : Exit F1 : General Help F2 : Previous Values F3 : : Optimized Defaults F4 : : Save & Ext Setup +K5 : : :: Croll help area opwards +K5 : :: Croll help area opwards	Mouse Right Cl Show Item Heli
TOR		Ok	3: Load Optim efault F4: Save & Exit 2

Except for the Home screen you may press the <F1> on any screen under the Setup Section to see a list of Hot Keys that are available.

The keyboard's Escape key <ESC> cancels the current screen and willtake you back to the previous screen.

How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS GUI Setup utility. This Setup utility can be accessed by pressing at the appropriate time during system boot.

Note: For the purposes of this manual, options that are printed in **Bold** are default settings.

How to Start the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the AMI BIOS GUI Setup Utility. From the Setup Home screen, you can access the other Setup Sections.

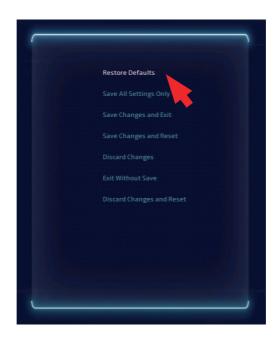
4-2 Saving and Loading

Save and Load

The Save and Load icon brings up a pop-up menu that enables the user to choose from different saving options at the end of the session:



Restore Defaults



To set this feature, select Restore Defaults from the Save & Load menu and click <OK>. These are factory settings designed for maximum system stability.

Save All Settings Only

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system and will not exit.

Save Changes and Exit

Select this option to save the current settings and exit Setup.

Save Changes and Reset

Select this option to save the current settings, exit Setup and reboot.

Discard Changes

Select this option discard any changes and return to Setup.

Exit without Save

Select this option to exit Setup without making any permanent changes to the system configuration.

Discard Changes and Reset

Select this option to discard all changes to setup and reboot.

Profiles



The Profiles icon brings up a pop-up menu that enables the user to choose from two saving and loading locations. These locations can be used to store new, or retrieve previously saved overclocking profiles:



Save Profile 1 / Save Profile 2

Select this option to save the current overclocking profile into either Profile 1 or Profile 2 location. Click "OK" when prompted, click "Cancel" to go back and not save.

Load Profile 1 / Load Profile 2

Select this option to load a previously saved overclocking profile from either Profile 1 or Profile 2 location. Click "OK" when prompted, click "Cancel" to go back and not load.

4-3 System Information

The System Information Screen displays the motherboard's configuration.

Motherboard

SYSTEM INFORMATION	Motherboard CPU	Memory	
PROCESSOR/ OPU	Supermicro C7Z1. BIOS Version		
NEWORY			
BOOTING			
INPUT/OUTPUT			
THE HAVE MONITOR			
			ESC: Exit
SHEPSET			
SECURITY	Profiles	SUPERMICR	Save & Load
		SUPERMICRO C7Z170-SQ	

The following information are displayed in this section:

- Motherboard Model Name C7Z170-SQ.
- **BIOS Version** this item displays the BIOS version number.
- Build Date and Time displays the BIOS build date and Time.

System Date

Click on the date to open the setup fields. This item sets and displays the system date. Click the up and down arrows to adjust the date.

System Time

Click on the time to open the setup fields. This item sets and displays the system time. Click the up and down arrows to adjust the system time.

CPU

PROCESSOR/ OPU	Intel(R) Core(TM) i7-6700K CPL CPU Signature		
		56 4000 MHz 800 MHz	
C NEWORY			
- BOOTIINE			
DIPUT/OUTPUT			
HAN NONITOR			
E WANAGEWENT			ESC: Exit
E CHIPSET			

The following information are be displayed in this section:

- **Type and Speed of CPU -** indicates the brand, model name, model number of the CPU and it's rated clock speed.
- **CPU Signature** displays the unique signature embedded in the CPU.
- Microcode Patch displays the CPU's microcode patch version.
- Max CPU Speed this item shows the maximum CPU speed.
- Min CPU Speed this item shows the minimum CPU speed.
- CPU Speed this item shows the current CPU speed.
- Processor Cores this item shows the detected number of processor cores.

Memory

SYSTEM INFORMATION	Motherboard CI	U Memory	
PROCESSOR/ OPU		1.7.0.0 2400 MHz	
		32768 MB (DDR4) 8192 MB 8192 MB	
C WENCRY	DIMM#81 DIMM#82		
BOOTENG			
DIFUT/OUTPUT			
R H/W MONITOR			
¢ WANLIGEWENT			
E CHIPSET			
SECURITY	Profiles	SUPERMICR	Save & Loan

The following information are be displayed in this section:

- **Memory Frequency** displays the system memory's detected speed.
- Total Memory shows the total detected system memory.
- DIMM#A1~DIMM#B2 displays the memory size detected on each memory slot.

4-4 Processor/CPU

Set all options for the processor in this section.

Information

PROCESSOR/ CPU	Intel(R) Core(TN JK CPU	@ 4.00GHz	
	CPU Signature		
	Microcode Patch		
A OVERCLOCKING			
KENORY			
+ BOOTING			
DNPUT/OUTPUT			
H/W MONITOR			
VANAGENEVT			
Ë CHIPSET			

The following CPU information will be displayed:

- **CPU Signature** displays the unique signature emebedded in CPU.
- Microcode Patch displays the CPU's microcode patch version.
- Max CPU Speed this item shows the maximum CPU speed.
- Min CPU Speed this item shows the minimum CPU speed.
- **CPU Speed** displays the CPU's rated speed.
- Processor Cores displays the number of cores the CPU has.
- Intel HT(Hyper-Threading) Technology indicates if Intel HT is supported.
- Intel VT-x Technology indicates if Intel VT-x is supported.
- Intel SMX Technology indicates if Intel SMX is supported.

- **64-bit** indicates if 64-bit is supported.
- **EIST Technology** displays whether EIST is supported or not.
- CPU C3 State indicates if ACPI C3 state is supported.
- CPU C6 State shows whether ACPI C6 state is supported or not.
- CPU C7 State indicates if ACPI C7 is supported.
- L1 Data Cache indicates if Level 1 cache is supported.
- L1 Code Cache displays if Level 1 code cache is supported.
- L2 Cache indicates if Level 2 cache is supported.
- L3 Cache displays whether Level 3 cache is supported or not.
- L4 Cache indicates if Level 4 cache is supported.

Performance

SYSTEM INFORMATION	Information Performance Power Management
PROCESSOR/ CPU	CPU Filex Ratio Override CPU Filex Ratio Settings
ZA OVEROLOCKENS	Hyper-threading (Toutive () Overclocking lock () Datable
E VENORY	Intel Virtualization Technology (ratifie) Hardware Prefetcher (ratifie) Adjacent Cache Line Prefetch (ratifie)
- 800TING	CPU AES (fundater) Boot Performance Mode (Turbe Performance (*)
DIPUT/OUTPUT	
72 H/N MONITOR	
	74. Save & Exit Setup 455: Exit
HE CHIPSET	
SECURITY	
	SUPERMICRO Copyright (C) 2015 American Magatranda, C7271 70-SQ

CPU Flex Ratio Override

Select Enabled to activate CPU Flex Ratio programming. The options are Enabled and **Disabled**.

CPU Flex Ratio Settings

When CPU Flex Ratio is enabled, this sets the value for the CPU Flex Ratio. The default is **35**. Press "+" or "-" on your keyboard to change the value.

Overclocking lock

Select Enabled to prevent the CPU overclocking. The options are Enabled and **Disabled**.

Intel® Virtualization Technology

(Available when supported by the CPU)

Select Enabled to use the Intel Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are Disabled and **Enabled**.

Hardware Prefetcher

(Available when supported by the CPU)

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disabled and **Enabled**.

Adjacent Cache Line Prefetch

(Available when supported by the CPU)

Select Enabled for the CPU to prefetch both cache lines for 128 bytes as comprised. Select Disabled for the CPU to prefetch both cache lines for 64 bytes. The options are Disabled and **Enabled**.

Note: If there is any change to this setting, you will need to power off and reboot the system for the change to take effect. Please refer to Intel's website for detailed information.

CPU AES

Select Enable for Intel CPU Advanced Encryption Standard (AES) Instructions support to enhance data integrity. The options are Disabled and **Enabled**.

Boot Performance Mode

This option enables the selection of the default CPU performance during system boot. The options are Power Saving, Max Non-Turbo Performance and **Turbo Performance**.

HardWare P States (HWP)

This option enables the Hardware P State support. The options are **Dis-abled** and Enabled.

Power Management

SYSTEM INFORMATION	Information Performant	e Power Management	and the second second
PROCESSOR/ OPU	Intel(R) SpeedStep(tm)	(Enabled ()	
	Turbo Mode	(Enabled (0)	
		Enabled ()	
		(Enabled ()	
		(1 and (3)	
CC NEWORY		C1 and C3	
BOOTING		(O) Disabled	
		(Enabled ()	
DIPUT/OUTPUT		(AUTO (V)	
A Theoryconcol			
E H/W MONITOR			
t vanligevent			
Ë CHIPSET	6		
SECURITY	Profiles	SUPERMICR	Save & Lo
		SUPERMICRO C7Z170-SO	

Intel(R) SpeedStep(tm)

Intel SpeedStep Technology allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. **Please refer to Intel's web site for detailed information.** The options are Disabled and **Enabled**.

Turbo Mode

This feature allows processor cores to run faster than the frequency recommended by the manufacturer. The options are Disabled and **Enabled**. If this feature is set to Enabled, the following items will display:

CPU C States

C-States architecture, a processor power management platform developed by Intel, can further reduce power consumption from the basic C1 (Halt State) state that blocks clock cycles to the CPU. Select Enabled for CPU C Sates support. The options are **Enabled** and Disabled. If this feature is set to Enabled, the following items will display:

Enhanced C-States

(Available when "CPU C States" is set to Enabled)

Select Enabled to enable Enhanced C1 Power State to boost system performance. The options are **Enabled** and Disabled.

C-State Auto Demotion

When this item is enabled, the CPU will conditionally demote C State based on un-cored auto-demote information. The options are Disabled, C1, C3 and **C1 and C3**.

C1

When this item is selected, the CPU will conditionally demote C3, C6 or C7 requests to C1 State based on un-cored auto-demote information.

С3

When this item is selected, the CPU will conditionally demote C6 or C7 requests to C3 State based on un-cored auto-demote information.

C-State Un-demotion

When this item is enabled, the CPU will conditionally undemote from demoted C3 or C1. The options are Disabled, C1, C3 and **C1 and C3**.

Package C-State Demotion

This item enables the Package C-State demotion. The options are **Dis-abled** and Enabled.

Package C-State Un-Demotion

When set, the CPU will conditionally undemote from demoted Packaged Package C-State Un-Demotion. The options are **Disabled** and Enabled.

CState Pre-Wake

When set, this option enables or disables the C-State pre wake. The options are **Enabled** and Disabled.

Package C State limit

Select Auto for the AMI BIOS to automatically set the limit on the C-State package register. The options are C0, C2, C3, C6, C7, C7s and **Auto**.

CPU Thermal Configuration

This section configures the motherboard's onboard Thermal Management.

CPU DTS

When this item is enabled, the motherboard's ACPI Thermal Management uses the DTS SMM mechanism to obtain CPU temperature values. When disabled, the ACPI Thermal Management uses the EC reported temperature values. The options are Enabled and **Disabled.**

ACPI 3.0 T-States

Use this feature to enable or disable the ACPI 3.0 T-States. The options are Enabled and **Disabled.**

4-5 Overclocking

Set all options for Overclocking in this section.

CPU Overclocking

PROCESSOR/ CPU	BCLK Clock Fre (1/100 MHz)		
	FCLK Frequency for y Power On	(1GHz (V)	
	Active Processor Cores		
A OVERCLOCKING	Load SMC CPU OC Setting	(Manual (V)	
	1-Core Ratio Limit Override		
E NEWORY			
+ BOOTING		Disabled (V)	
		Turbo Performance	
DIPUT/OUTPUT		(Enabled ()	
		(Enabled (•)	
		(0) Disabled	
R H/W MONITOR			
CF WANLIGENENT			
	Power Limit 1 Window	(<u>s</u> (v)	
	States in succession	and the second	
CHIPSET			

BCLK Clock Frequency(1/100 Mhz)

Use this feature to select the base clock frequency. Press "+" or "-" on your keyboard to change the value. Enter 10000 to use the manufacturer's default setting.

FCLK Frequency for Early Power On

Use this feature to select the f-clock frequency. The options are Normal (800Mhz), **1GHz** and 400MHz.

Active Processor Cores

Use this feature to select the number of active processor cores. The options are **All**, 1, 2, 3 and 4 (These options depend on how many cores are supported by the CPU.)

Load SMC CPU OC Setting

This item has optimized pre-configured overclock settings. Select one to activate. The options are **Manual**, 3.5GHz \sim 5.0GHz (in 100MHz increments).

1-Core Ratio Limit Override

This increases (multiplies) 1 clock speed in the CPU core in relation to the bus speed when one CPU core is active. Press "+" or "-" on your keyboard to change the value. Enter 39 to use the manufacturer's default setting.

2-Core Ratio Limit Override

This increases (multiplies) 2 clock speeds in the CPU core in relation to the bus speed when two CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 38 to use the manufacturer's default setting.

3-Core Ratio Limit Override

This increases (multiplies) 3 clock speeds in the CPU core in relation to the bus speed when three CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 37 to use the manufacturer's default setting.

4-Core Ratio Limit Override

This increases (multiplies) 4 clock speeds in the CPU core in relation to the bus speed when four CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 36 to use the manufacturer's default setting.

RSR

This item enables or disables the RSR feature. The options are **Disabled** and Enabled.

Boot Performance Mode

This option enables the selection of the default CPU performance during system boot. The options are Max Non-Turbo Performance, Max Battery and **Turbo Performance**.

Intel(R) SpeedStep(tm) Technology

Enhanced Intel SpeedStep Technology (EIST) allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. **Please refer to Intel's web site for detailed information.** The options are Disabled and **Enabled**.

Turbo Mode

This feature allows processor cores to run faster than the frequency recommended by the manufacturer. The options are Disabled and **Enabled**. If this feature is set to Enabled, the following items will display:

Package Power Limit MSR Lock

This feature enables or disables the locking of Package Power Limit settings. When enabled Package Power Limit MSR will be locked and a reset will be required to unlock the register. The options are **Disabled** and Enabled.

Power Limit 1 Override

This feature disables or enables the Power Limit 1 Override. If this option is disabled, the BIOS will program the default values for Power Limit and Power Limit 1 Time Window. The options are Disabled and **Enabled.**

Power Limit 1

This feature configures Package Power Limit 1, in milliwatts. When the limit is exceeded, the CPU ratio is lowered after a period of time (see item below). A lower limit can save power and protect the CPU, while a higher limit improves performance. This value must be between Min Power Limit TDP limit. If value is '0' the BIOS will program the TDP value. Use the number keys on your keyboard to enter the value. The default setting is dependent on the CPU.

Power Limit Window

This item determines how long the time window over which the TDP value is maintained. Use the number keys on your keyboard to enter the value. The default setting is $\mathbf{8}$. This value may vary between 0~128.

Power Limit 2 Override

This feature disables or enables the Power Limit 2 Override. If this option is disabled, the BIOS will program the default values for Power Limit and Power Limit 2 Time Window. The options are Disabled and **Enabled**.

Power Limit 2

This feature configures Package Power Limit 2, in milliwatts. When the limit is exceeded, the CPU ratio is lowered after a period of time (see item below). A lower limit can save power and protect the CPU, while a higher limit improves performance. This value must be between Min Power Limit TDP limit. If value is '0' the BIOS will program the TDP value. Use the number keys on your keyboard to enter the value. The default setting is dependent on the CPU.

Platform PL1 Enable

This option disables or enables the Platform Power Limit 1 programming. If this option is enabled, it activates the PL1 value to be used by the processor to limit the average power of the given time window. The options are Disabled and **Enabled.** When enabled, the following will appear:

Platform PL1 Power

This feature configures Platform PL1 Power. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacturer's default setting.

Platform PL1 Window

This item determines how long the time window over which the TDP value is maintained. Use the number keys on your keyboard to enter the value. The default setting is $\mathbf{0}$. This value may vary between $0 \sim 128$.

Platform PL2 Enable

This optioin disables or enables the Platform Power Limit 2 programming. If this option is enabled, it activates the PL1 value to be used by the processor to limit the average power of the given time window. The options are **Disabled** and Enabled. When enabled, the following will appear:

Platform PL2 Power

This feature configures Platform PL2 Power. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacturer's default setting.

Power Limit 3 Override

This feature disables or enables the Power Limit 3 Override. If this option is disabled, the BIOS will program the default values for Power Limit and Power Limit 3 Time Window. The options are **Disabled** and Enabled. If enabled, the following will appear:

Power Limit 3

This feature configures Package Power Limit 3, in milliwatts. When the limit is exceeded, the CPU ratio is lowered after a period of time (see item below). A lower limit can save power and protect the CPU, while a higher limit improves performance. This value must be between Min Power Limit TDP limit. If value is '0' the BIOS will program the TDP value. Use the number keys on your keyboard to enter the value. The default setting is dependent on the CPU.

Power Limit 3 Window

This item determines how long the time window over which the TDP value is maintained. Use the number keys on your keyboard to enter the value. The default setting is $\mathbf{0}$. This value may vary between $0 \sim 128$.

Power Limit 3 Duty Cycle

This item allows the user to determine the percentage of time the CPU should operate at the power set by the user for the item above (i.e., If Power Limit 3 Time is set at 100 secs, a 60% duty cycle means the CPU will run 60 seconds at the power limit set in Power Limit 3 every 100 seconds.). Use the number keys on your keyboard to enter the value between $1 \sim 100$. Enter 0 to use the manufacturer's default setting.

Power Limit 3 Lock

This feature enables or disables the locking of Power Limit 3 settings. When enabled Power Limit 3 settings will be locked and a reset will be required to unlock the register. The options are **Disabled** and Enabled.

CPU Flex Ratio Override

Select Enabled to activate CPU Flex Ratio programming. The options are **Disabled** and Enabled.

CPU Flex Ratio Settings

When CPU Flex Ratio is enabled, this sets the value for the CPU Flex Ratio. The default is **35**. Press "+" or "-" on your keyboard to change the value.

CPU Max OC Ratio

This option sets the maximum overclocking ratio for the CPU core. The allowable range is from $0 \sim 80$. Press "+" or "-" on your keyboard to change the value.

System Agent Voltage (mV)

This option sets the System Agent Voltage. Press "+" or "-" on your keyboard to change the value.

Core Voltage Mode

Use this feature to select the Core voltage mode. The options are Override and **Adaptive**.

Core Extra Turbo Voltage

This option specifies the extra voltage when IA Core is running in Turbo mode. Range is from $0\sim2000$ mV. Press "+" or "-" on your keyboard to change the value.

Core Voltage Offset (mV)

Use this feature to set the CPU Voltage Offset value from -500mV to +500mV. Enter **0** to use the manufacture default value. Press "+" or "-" on your keyboard to change the value.

Offset Prefix

Use this feature to set the Core Voltage Offset value as a positive (+) number or a negative (-) number. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

Core PLL Voltage Offset

Use this feature to set the Core PLL Voltage Offset value. Press "+" or "-" on your keyboard to make a selection.

Ring Max OC Ratio

This option sets the maximum overclocking ratio for the RING domain. Press "+" or "-" on your keyboard to change the value.

Ring Min OC Ratio

This option sets the minimum overclocking ratio for the RING domain. Press "+" or "-" on your keyboard to change the value.

Ring Voltage Mode

Use this feature to select the Ring voltage mode. The options are Override and **Adaptive**.

Ring Extra Turbo Voltage

Use this feature to set the Ring Extra Turbo Voltage value. Press "+" or "-" on your keyboard to change the value.

Ring Voltage Offset

Use this feature to set the CPU Voltage Offset value from -1000mV to +1000mV. Enter ${\bf 0}$ to use the manufacture default value. Press "+" or "-" on your keyboard to change the value.

Offset Prefix

Use this feature to set the Ring Offset value as a positive (+) number or a negative (-) number. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

Uncore Voltage Offset

Use this feature to set the ncore domain Voltage Offset value from -1000mV to +1000mV. Enter **0** to use the manufacture default value. Press "+" or "-" on your keyboard to change the value.

Offset Prefix

Use this feature to set the offset value as a positive (+) number or a negative (-) number. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

PCH 1.0V

Use this feature to set the PCH voltage. The options are **1.000V**, 1.063V, 1.103V, 1.158V, 1.216V, 1.257V and 1.1315V.

VCCPLL_IO

Use this feature to set the processor PLL IO. The options are **0.975V**, 1.163V and 1.315V.

VCCPLL_OC

Use this feature to set the processor PLL overclocking power rail. The options are **1.210V**, 1.260V, 1.310V, 1.360V, 1.410V, 1.460 and 1.510V.

Load Line Calibration

Load line calibration is vDroop, which is the tendency for a CPU's vCore to drop when going from an idle state to a load state. Enable this feature to reduce vDroop. The options are Disabled and **Enabled**.

VRM Total Current Limit Capability

Use this feature to set the CPU integrated voltage regulator when under extreme high load for VRM total current limit. The options are **100%**, 110%, 120%, 130%, 140%, 150%, 160%, 170%, 180% and 190%.

VRM Per-Phase Current Limit Capability

Use this feature to set the CPU integrated voltage regulator when under extreme high load for VRM per-phase current limit. The options are **100%**, 110%, 120%, 130%, 140%, 150%, 160%, 170% and 180%.

PMON Slope

This option sets the PMON Slope value in 1/100 increments. For a 1.25 slope, enter 125. Press "+" or "-" on your keyboard to change the value.

PMON Offset

Use this feature to set the PMON offset value. Range is -255. Enter '0' for auto. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

PSYS PMax Current

This option sets the PSYS PMax Current in 1/8A increments. Range is from $0 \sim 8192$. For a PMAX of 125A, enter 1000. Enter '0' for auto. Press "+" or "-" on your keyboard to change the value.

System Agent VR Settings

This option sets the System Agent VR Settings. Press "+" or "-" on your keyboard to change the value.

Core/IA VR Settings

This option sets the Core IA VR Settings. Press "+" or "-" on your keyboard to change the value.

Ring VR Settings

This option sets the Ring VR Settings. Press "+" or "-" on your keyboard to change the value.

Memory Overclocking

SYSTEM INFORMATION	CPU OverClocking Memory@ver	Clocking Graphics OverClocking	
PROCESSOR/ OPU	Memory Profile: tCK [fs]	L Custom XMP1 XMP2 95 A. 23333 750000 714000	
	tCL tRCD/tRP tRAS	15 15 16 16 15 15 18 18 36 36 35 37	
III NENORY	tCWL tFAW tREFI		
BOOTENG			
DIFUT/OUTFUT			
C H/W MONITOR		Custom profile (V) 133 (V) Disabled (V)	
XANUSENBAL	Memory Frequency	(DDR-2400MHz (V)	ESC: Exit
E CHIPSET			
SECURITY	Profiles	SUPERMICR	Save & Lo

Load Memory Profiles

Use this feature to set Performance Memory Profiles which may cause impact on memory behavior*. Select Automatic to allow the BIOS to automatically set Performance Memory Profiles. Select Manual to manually configure Performance Profiles. The options are **Default**, Custom, XMP 1 and XMP 2.

If Default is selected, the installed memory will run at 2200MHz if the detected memory is rated at 2400MHz or above, and run at 1867MHz if the memory detected is rated at 1867MHz.

If Custom, XMP 1 or XMP 2 is selected, the following options will be available. These options will only appear if XMP (Extreme Memory Profile) Memory is/are installed:

Memory Timing Configuration (Expert Mode)

tCK [fs]

This option configures the clock cycle time. The default is **938000**.

tCL

This option configures the Cas Latency Range. Enter a number between 4-18. The default is **15**.

tRCD/tRP

This option selects the Ras Precharge Range and Row to Col Delay Range. Enter a number between 1-38. The default is **15**.

tRAS

This option selects the Ras Active Time. Enter a number between 1-586. The default is ${\bf 36}.$

tCWL

This option selects the Minimum CAS Write Latency Time. Enter a numeric value. The default is ${f 8}.$

tFAW

This option selects the Minimum Four Activate Window Delay Time. Enter a numeric value between 1-586. The default is **23**.

tREFI

This option configures the Maximum tREFI Time (Average Periodic Refrech Interval). Enter a numeric value. The default is **6240**.

tRFC

This option selects the Minimum Refresh Recovery Delay Time. Enter a number between 1-9363. The default is **278**.

tRRD

This option selects the Minimum Row Active To Row Active Delay Time. Enter a number between 1-38. The default is ${\bf 4}$.

tRTP

This option configures the Internal Read to Precharge Command Delay Time. Enter a number between 1-38. The default is ${\bf 8}$.

tWR

This option configures the Minimum Write Recovery Time. Enter a number between 1-38. The default is **16**.

tWTR

This option configures the Minimum Internal Write to Read Command Delay Time. Enter a number between 1-38. The default is ${f 0}$.

NMode

This option configures NMode. The default is 2.

VDD (mV)

This option configures NMode. The default is **1200**. In millivolts.

DIMM Profile

This option selects the DIMM timing profile. The options are **Default DIMM Profile** and Custom Profile.

If Custom Profile is selected, the following options appear:

Memory Reference Clock

This option selects the Memory Clock ratio. The options are ${\bf 133MHz},$ 100MHz and Auto.

QCLK Odd Ratio

This option enables or disables the quadrature clock odd ratio. The options are **Disabled**, and Enabled.

Memory Frequency

This option selects the type/speed of the memory installed. The options are DDR4-1066MHz, DDR4-1333MHz, DDR4-1600MHz, DDR4-1800MHz, DDR4-2000MHz, DDR4-2000HHz, DDR4-2400MHz, and DDR4-2600MHz. **Default speed is auto detected.**

DRAM CH_A/B Voltage

This option selects the DRAM channel A/B voltage. The options are 1.20V, 1.25V, 1.30V, 1.35V, 1.40V, 1.45V, 1.50V and 1.52V.

tCL

This option configures the Cas Latency Range. Enter a number between 4-18. The default is **15**.

tRCD/tRP

This option selects the Row to Col Delay Range and Ras Precharge Range. Enter a number between 1-38. The default is **15**.

tRAS

This option selects the Ras Active Time. Enter a number between 1-586. The default is ${\bf 36}$.

Minimum CAS Write Latency Time (tCWL)

This option selects the Minimum CAS Write Latency Time. Enter a numeric value. The default is **14**.

tFAW

This option selects the Minimum Four Activate Window Delay Time. Enter a numeric value between 1-586. The default is **23**.

Maximum tREFI Time (tREFI)

This option configures the Maximum tREFI Time (Average Periodic Refrech Interval). Enter a numeric value. The default is **8316**.

tRFC

This option selects the Minimum Refresh Recovery Delay Time. Enter a number between 1-9363. The default is **278**.

tRRD

This option selects the Minimum Row Active To Row Active Delay Time. Enter a number between 1-38. The default is **4**.

tRTP

This option configures the Internal Read to Precharge Command Delay Time. Enter a number between 1-38. The default is ${\bf 8}$.

tWR

This option configures the Minimum Write Recovery Time. Enter a number between 1-38. The default is **16**.

tWTR

This option configures the Minimum Internal Write to Read Command Delay Time. Enter a number between 1-38. The default is ${f 0}$.

NMode

This option configures NMode. The default is 2.

3rd Timing:

tRPab_ext

This option configures the tRPab_ext. Enter a numeric value. The default is $\boldsymbol{0}.$

tRDPRE

This option configures the tRDPRE. Enter a numeric value. The default is ${\boldsymbol 8}.$

tWRPRE

This option configures the tWRPRE. Enter a numeric value. The default is ${\bf 34}.$

tRRD_sg

This option configures the tRRD_sg. Enter a numeric value. The default is ${\bf 6}.$

tRRD_dg

This option configures the tRRD_dg. Enter a numeric value. The default is ${\bf 4}.$

Derating ext

This option configures the derating_ext. Enter a numeric value. The default is ${\bf 2}.$

ODT_read_duration

This option configures the ODT Read Duration. Enter a numeric value. The default is ${\bf 0}.$

ODT_Read_Delay

This option configures the ODT Read Delay. Enter a numeric value. The default is ${\bf 1}.$

ODT_write_duration

This option configures the ODT Write Duration. Enter a numeric value. The default is ${\bf 0}.$

ODT_Write_Delay

This option configures the ODT Write Delay. Enter a numeric value. The default is ${\bf 0}.$

Write_Early_ODT

This option configures the Write Early ODT. Enter a numeric value. The default is ${\bf 0}.$

tAONPD

This option configures the tAONPD. The default is 10.

ODT_Always_Rank0

This option configures the ODT Always Rank0. Enter a numeric value. The default is ${\bf 0}.$

tRDRD_sg

This option configures the between module read to read delay (tRDRD_sg). Enter a numeric value. The default is **6**.

tRDRD_dg

This option configures the between module read to read delay (tRDRD_dg). Enter a numeric value. The default is **4**.

tRDRD_dr

This option configures the between module read to read delay $(tRDRD_dr)$. Enter a numeric value. The default is **6**.

tRDRD_dd

This option configures the between module read to read delay (tRDRD_dd). Enter a numeric value. The default is **7**.

tRDWR_sg

This option configures the between module read to write delay (tRDWR_sg). Enter a numeric value. The default is **10**.

tRDWR_dg

This option configures the between module read to write delay (tRDWR_dg). Enter a numeric value. The default is **10**.

tRDWR_dr

This option configures the between module read to write delay (tRDWR_dr). Enter a numeric value. The default is **10**.

tRDWR_dd

This option configures the between module read to write delay (tRDWR_dd). Enter a numeric value. The default is **11**.

tWRRD_sg

This option configures the between module read to write delay (tWRRD_sg). Enter a numeric value. The default is **28**.

tWRRD_dg

This option configures the between module read to write delay (tWRRD_dg). Enter a numeric value. The default is **23**.

tWRRD_dr

This option configures the between module read to write delay (tWRRD_dr). Enter a numeric value. The default is **6**.

tWRRD_dd

This option configures the between module read to write delay (tWRRD_dd). Enter a numeric value. The default is **6**.

tWRWR_sg

This option configures the between module read to write delay (tWRWR_sg). Enter a numeric value. The default is **6**.

tWRWR_dg

This option configures the between module read to write delay (tWRWR_dg). Enter a numeric value. The default is **4**.

tWRWR_dr

This option configures the between module read to write delay (tWRWR_dr). Enter a numeric value. The default is **7**.

tWRWR_dd

This option configures the between module read to write delay (tWRWR_dd). Enter a numeric value. The default is **7**.

tXP

This option configures tXP. Enter a numeric value. The default is **7**.

tXPDLL

This option configures tXPDLL. Enter a numeric value. The default is ${\bf 26}.$

tPRPDEN

This option configures tPRPDEN. Enter a numeric value. The default is **2**.

tRDPDEN

This option configures tRDPDEN. Enter a numeric value. The default is **20**.

tWRPDEN

This option configures tWRPDEN. Enter a numeric value. The default is ${\bf 34}.$

DIIBwEn[0]

This option configures DIIBwEn[0]. Enter a numeric value. The default is $\boldsymbol{0}.$

DIIBwEn[1]

This option configures DIIBwEn[1]. Enter a numeric value. The default is ${\bf 1}.$

DIIBwEn[2]

This option configures DIIBwEn[2]. Enter a numeric value. The default is ${\bf 2}$.

DIIBwEn[3]

This option configures DIIBwEn[3]. Enter a numeric value. The default is ${\bf 2}.$

Graphics OverClocking

PROCESSOR/ CPU	GT Slice Domain		
PHOLESSUN UPU	GT OverClocking Frequency	•	
	GT Voltage Mode	Adaptive (V)	
I NEWORY		(•(v)	
	GT Unslice Domain		
		0	
SCOTING		(Adaptive (7)	
	GT Voltage Offset		
DIPUT/OUTPUT		(T)	
H/W NONETOR			
KANAGENENT	-		
CHIPSET	-		

GT Slice Domain

GT OverClocking Frequency

This option selects the Overclocked RPO frequency in multiples of 50MHz. The default is ${\bf 0}.$

GT Voltage Mode

Use this feature to select the Overclocking GT mode. The options are Override, Offset and **Adaptive**.

GT Extra Turbo Voltage Target

(If Adaptive is selected above) Use this feature to set the extra voltage applied while GT is operating in trubo mode. Specify a value from 0mV to 2000mV. Enter **0** to use the manufacture default value.

GT Voltage Offset

(If Offset is selected above) Use this feature to set the GT Adaptive voltage Target(mV) value from 0mV to 2000mV. Enter ${\bf 0}$ to use the manufacture default value.

Offset Prefix

Use this feature to set the Offset value as a positive (+) number or a negative (-) number. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

GT Unslice Domain

GT OverClocking Frequency

This option selects the Overclocked RPO frequency in multiples of 50MHz. The default is ${\bf 0}.$

GT Voltage Mode

Use this feature to select the Overclocking GT mode. The options are Override, Offset and **Adaptive**.

GT Extra Turbo Voltage Target

(If Adaptive is selected above) Use this feature to set the extra voltage applied while GT is operating in trubo mode. Specify a value from 0mV to 2000mV. Enter **0** to use the manufacture default value.

GT Voltage Offset

(If Offset is selected above) Use this feature to set the GT Adaptive voltage Target(mV) value from 0mV to 2000mV. Enter ${\bm 0}$ to use the manufacture default value.

Offset Prefix

Use this feature to set the Offset value as a positive (+) number or a negative (-) number. Press "+" or "-" on your keyboard to make a selection. The default setting is "+".

GT-UnSliced VR Settings

This option configures the GT UnSliced VR setting. The default is ${\bf 0}.$

GT-Sliced VR Settings

This option configures the GT Sliced VR setting. The default is $\boldsymbol{0}.$

4-6 Memory

Set all options for the Memory in this section.

Memory Information

	Memory Informa			
PROCESSOR/ OPU	Memory RC Vers Memory Frequency	1.7.0.0 2400 MHz	. 0	
27 OVEROLOCKING				
Ú NEHOSI.				
BOOTING	(tCL-tRCD-tRP-tRAS)			
DIPUT/OUTPUT				
RAN MONITOR				
				LESC: Exit
HE CHIPSET	Profiles	SUPERMICRO		Save & Load
SECURITY		SUPERMICRO C7Z170-SQ		Copyright (C) 2015 American Megatrends,

This item displays the following information on the memory modules installed on the motherboard.

- **Memory RC Version** this item displays the memory controller version.
- **Memory Frequency** this item displays the frequency of the installed memory.
- Total Memory this item displays the total memory size detected.
- VDD (Memory Voltage) this item displays the current memory voltage.
- **DIMM#A1** displays if a DIMM is installed on this slot, and the DIMM size.
- **DIMM#A2** displays if a DIMM is installed on this slot, and the DIMM size.
- **DIMM#B1** displays if a DIMM is installed on this slot, and the DIMM size.

- **DIMM#B2** displays if a DIMM is installed on this slot, and the DIMM size.
- Memory Timings (tCL-tRCD-tRP-tRAS) displays memory timings.

Memory Configuration

	Phenory Information Phenory Confe	
PROCESSOR/ CPU	Maximum Memory Frequency Max TOLUD (Spein (*)	
	Memory Scrambler Ended () Memory Remap Ended ()	
THE MEMORY	Fast Boot (name (*)	
- BOOTING		
DIPUT/OUTPUT		
🛛 HAN NONITOR		
: CHIPSET		
SECURITY		
	SUPERMICRO Copyright (C) 3015 American Megatr C72170-SQ	

This section is used to configure the memory modules installed on the motherboard.

Maximum Memory Frequency

Use this feature to select the memory frequency. Select Auto for the system to automatically detect the memory frequency. The options are **Auto**, 1067, 1333, 1600, 1867, 2133, 2400, 2667, 2933 and 3200.

Max TOLUD (Top of Low Usable DRAM)

This feature sets the maximum TOLUD value, which specifies the "Top of Low Usable DRAM" memory space to be used by internal graphics devices, GTT Stolen Memory, and TSEG, respectively, if these devices are enabled. The options are **Dynamic**, 1 GB, 1.25 GB, 1.5 GB, 1.75 GB, 2 GB, 2.25 GB, 2.5 GB, 2.75 GB, 3 GB, 3.25 GB and 3.5 GB.

Note: TSEG is a block of memory that is only accessible by the processor while operating in System Management Mode (SMM).

Memory Scrambler

This feature enables or disables memory scrambler support for memory error correction. The settings are **Enabled** and Disabled.

Memory Remap Feature

PCI memory resources will overlap with the total physical memory if 4GB of memory or above is installed on the motherboard. When this occurs, **Enable** this function to reallocate the overlapped physical memory to a location above the total physical memory to resolve the memory overlapping situation. The options are **Enabled** and Disabled.

Fast Boot

This option sets fast system boot, quick POST, etc. The options are **En-abled** and Disabled.

4-7 Booting

Set all options for the Boot devices in this section.

Boot Device Settings



Quiet Boot

This option sets the state to which the system buzzer is configured when booting. The options are **Enabled** and Disabled.

Boot Mode Select

This option sets the boot mode between Legacy BIOS, UEFI BIOS or Dual. The options are Dual, **Legacy** and UEFI.

Legacy Boot Order #1~#7

This option sets the order of which the system boots from the installed boot devices. The options and defaults are dependent on the boot devices detected.

Delete Driver Option

Use this option to delete and EFI driver from the boot order.

NETWORK Drive BBS Priorities

Use this option to specify the boot device priority sequence from available network drives.

BIOS Features

SYSTEM INFORMATION	Boot Device Settings BIOS Featu	nes		
2 PROCESSOR/ OPU		1 (On	()	
		Enabled Disabled	(v) (v)	
III NENCRY		(Instant Off Last State (0) Disabled)	(v) (v)	
BOOTENG				
DIPUT/OUTPUT				
RE HAN MONITOR				
¢ wawaenbal				F4: Save & Exit Setup
E CHIPSET	6			5
SECURITY	Profiles	SUPERMICR		Save & Loa

Setup Prompt Timeout

This feature sets the length of time the BIOS waits for the key to be pressed by the user when entering setup. Enter a value in seconds.

Bootup Numlock State

This option sets the state to which the NumLock key is configured when booting. The options are **Enabled** and Disabled.

Wait for "F1" for Error

This option sets whether the system will wait for the user to press "F1" when an error is detected during POST. The options are **Enabled** and Disabled.

Retry Boot

Select Enabled to force the system to reboot when system fails to boot. The options are **Disabled** and Enabled.

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are **Instant Off** and 4 Seconds Override.

AC Loss Policy

Use this feature to set the power state after a power outage. Select Power-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are **Last State**, Power On, Stay Off.

EUP Support

EuP, or Energy Using Product is a European energy-saving specification that sets a standard on the maximum total power consumption on electrical products. Check the box to activate EUP support. The default is Unchecked (**Disabled**).

4-8 Input/Output

Set all options for the motherboard I/O in this section.

SATA

PROCESSOR/ OPU	SATA Controll	(Enabled ()		
	SATA Mode Selectio	AHCI	()	
T OVERCLOCKING	Serial ATA Port 0: Empty			
		Unknown		
		(O) Disabled		
I NEWORY		(O) Disabled		
			()	
	Serial ATA Port 1: Empty			
+ BOOTING				•
		(O) Disabled		
้า มายาวนั้นายนา		(O) Disabled		
DINPUT/OUTPUT		Hard Disk Drive	()	
	Serial ATA Port 2: Empty			
E H/W MONITOR				
		(O) Disabled		
		(O) Disabled		
VANAGENENT				
	SATA Device Type	Hard Disk Drive	(*)	
E CHIPSET				

SATA Controllers

This item Enables or Disables the built-in SATA controllers on the motherboard. The options are **Enabled** and Disabled.

SATA Mode Selection

This item selects the mode for the installed SATA drives. The options are IDE, **AHCI** and RAID.

SATA Controller Speed (AHCI or RAID Mode only)

This item selects the SATA Controller Speed. The options are **Default**, Gen1, Gen2 and Gen3.

If the item SATA Mode Selection above is set to AHCI, the following items are displayed:

Port 0 ~ Port 5

This item Enables or Disables SATA Ports $0 \sim 5$. The options are **Enabled** and Disabled.

Hot Plug

This feature designates the SATA port specified for hot plugging. Set this item to Enabled for hot-plugging support, which will allow the user to replace a SATA disk drive without shutting down the system. The options are **Disabled** and Enabled.

Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization.

SATA Device Type

This feature configures the selected SATA port to support either a solid state drive or hard disk drive. The options are **Hard Disk Drive** and Solid Sate Drive.

PCIe/PCI/PnP

) SYSTEM INFORMATION	SATA PCIe/PCI/PnP	USB Settings	PCH-FW	
	PCIe Slot Configuration			
E OVEROLOCKING				
C OVERILUXING				
I NEWORY	Option ROM execution			
e veziciti		Legacy	()	
		Legacy	()	
+ BOOTING	PCIe/PCI/PnP Configuration			
	PCH SLOT1 PCI-E 3.0 X1 (IN X4) OPROM	Legacy.	<u>(</u> v)	
INPUT/OUTPUT	CPU SLOT2 PCI-E 3.0 X4 (IN X16) OPROM	Legacy	(7)	
	PCH SLOT3 PCI-E 3.0 X4 OPROM	Legacy	()	Mouse Right Click Show Item Help
H/W MONITOR	CPU SLOT4 PCI-E 3.0 X8 (IN X16) OPROM	Legacy	<u>(</u> v)	
	PCH SLOT5 PCI-E 3.0 X1 (IN X4) OPROM	Legacy	(v)	
F WANLAGEWENT	CPU SLOTE PCI-E 3.0 X16 OPROM	Legacy	(▼) ->	
e Chilpset				-
SECURITY	Profiles	SUPERMICR		Save &

PEG Port Configuration

PCH SLOT5 PCI-E 3.0 X1 (IN X4), PCH SLOT3 PCI-E 3.0 X4, M.2 PCI-E X4, PCH SLOT1 PCI-E 3.0 X1 (IN X4)

PCI Express Root Port 20

This option controls the PCI Express root port. The options are **En-abled** and Disabled.

ASPM Support

Set this option to configure the Active State Power Management (ASPM) of the PEG device on this PCIE slot. The options are **Auto**, Disabled, LOs, L1, and LOsL1.

L1 Substates

Set this option to configure the PCI Express L1 substate setting. The options are Disabled, L1.1, L1.2 and **L1.1 & L1.2**.

PCIE Speed

Set this option to configure the speed setting of this PCIE slot . The options are **Auto**, Gen1, Gen2 and Gen3.

Detect Non-Compliant Device

Set this option to configure the motherboard to detect any noncompliant PCIE device installed on any of the PCIE slots. The options are Enabled and **Disabled**.

PCIE LTR Lock

Set this option to configure PCIE LTR Configuration Lock. The options are Enabled and **Disabled**.

Option ROM Execution

Video

This feature controls which option ROM to execute for the Video device. The options are Do Not Launch, UEFI and **Legacy**.

Storage Option ROM/UEFI Driver

Use this feature to select the storage option ROM/UEFI driver type. The options are Disabled, EFI, and **Legacy**.

PCIe/PCI/PnP Configuration

PCH SLOT1 PCI-E 3.0 X1 (IN X4) OPROM, CPU SLOT2 PCI-E 3.0 X4 (IN X16) OPROM, PCH SLOT3 PCI-E 3.0 X4 OPROM, CPU SLOT4 PCI-E 3.0 X8 (IN X16) OPROM, PCH SLOT5 PCI-E 3.0 X1 (IN X4) OPROM, CPU SLOT6 PCI-E 3.0 X16 OPROM

Select Disabled to deactivate the selected slot, Legacy to activate the slot in legacy mode and EFI to activate the slot in EFI mode. The options are Disabled, **Legacy**, and EFI.

Onboard LAN Option ROM Type

Select the onboard LAN option ROM type. The options are $\ensuremath{\text{Legacy}}$ and EFI.

Onboard LAN1 Option ROM

Select PXE (Preboot Execution Environment) to boot the computer using a PXE device installed in a LAN port specified. Select Disabled to prevent system boot using a device installed in a LAN port. The options are **Disabled** and PXE.

Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are Disabled and **Enabled**.

Ipv4 PXE Support (Available when Network Stack is set to Enabled)

Select Enabled to enable Ipv4 PXE (Preboot Execution Environment) for boot support. If this feature is set to Disabled, Ipv4 PXE boot option will not be supported. The options are **Enabled** and Disabled.

Ipv6 PXE Support (Available when Network Stack is set to Enabled)

Select Enabled to enable Ipv6 PXE (Preboot Execution Environment) for boot support. If this feature is set to Disabled, Ipv6 PXE boot option will not be supported. The options are Enabled and **Disabled**.

USB Settings



- USB Controllers displays the detected USB controllers.
- **USB Devices** displays the detected USB devices.

Legacy USB Support

This item activates Legacy USB Support. The options are **Enabled** and Disabled.

EHCI Support

This item activates EHCI Support. The options are **Enabled** and Disabled.

XHCI Support

This item activates XHCI Support. The options are **Enabled** and Disabled.

USB 3.1 Support

This item activates USB 3.1 Support. The options are $\ensuremath{\textbf{Enabled}}$ and Disabled.

Install Windows 7 USB Support

Enable this feature to use the USB keyboard and mouse during the Windows 7 installation, since the native XHCI driver support is unavailable. Use a SATA optical drive as a USB drive, and USB CD/DVD drives are not supported. Disable this feature after the XHCI driver has been installed in Windows. The options are **Disabled** and Enabled.

PCH-FW (Firmware)

) SYSTEM INFORMATION			PCH-FW	Super IO Configuration
PROCESSOR/ CPU				
A OVEROLOCKING		Consumer SKU Disabled	<u>(</u> †)	
I NEMORY				
BOOTING				
DIPUT/OUTPUT				
B H/W MONITOR				
KANAGEWENT				ESC: Exit
E CHEPSET		<u> </u>		
SECURITY	Profiles	SUPERMICR	•	Save &

The following information for the PCH Firmware.

- **ME FW Version** displays the Management Engine version number.
- **ME Firmware Mode** displays the Management Engine mode.
- **ME Firmware Type** displays the Management Engine firmware type.
- ME Firmware SKU displays the Management Engine SKU size.

ME FW Image Re-Flash

This item will update the PCH Firmware from an omage in a USB Flashdrive attached to a USB port. The options are **Enabled** and Disabled.

Super IO Configuration

() SYSTEM INFORMATION		I/PnP USB Settings	PCH-FW	Super IO Configuration
	Serial Port 1 Configuration Serial Port	(Enabled ()		
		IO=3F8h; IRQ=4;	<u>(</u> †)	
TTT: NEWORY				
BOOTING				
S INFUT/OUTFUT				
R HAN MONITOR				
				F4: Save & Exit Setup
a Chipset	6	<u> </u>		10 Y
SECURITY	Profiles	SUPERMICR		Save & Load

Serial Port 1 Configuration

Serial Port

This item will Enable or Disable Serial Port 1 (COM1). Place a tick mark on the box to enable Serial Port 1. The default is **Enabled**.

Device Settings

This item displays the current IRQ setting for Serial Port 1 (COM1).

IRQ Settings

This item configures the IRQ setting for Serial Port 1 (COM1). The options for Serial Port 1 are **Auto**, IO=3F8h; IRQ=4, IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12, IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12 and IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12.

4-9 Hardware Monitor

Hardware Monitors for various sensors on the motherboard are in this section.

Temperature

SYSTEM INFORMATION	Temperature Fan Spee	d Voltage		
Si	PU Temperati ystem Temperi ya			
	eripheral Temperature CH Temperature			
TT VENORY				
- 800TING				
DIPUT/OUTPUT				
zzi hva monifice				
				E4: Save & Exit Setup ESC: Exit
E CHIPSET	6			
	files	SUPERM	ICRO	Save & La

The following items will be displayed:

- CPU Temperature displays the CPU temperature detected by PECI.
- System Temperature indicates the system internal temperature.
- **Peripheral Temperature** displays the detected peripheral device temperature.
- PCH Temperature indicates the detected PCH chip temperature.

Fan Speed

SYSTEM INFORMATION	Temperature Fag Sp	red Voltage		
PROCESSOR/ OPU		: N/A		
	Fan3 Speed Fan4 Speed Fan5 Speed Fan Speed Control Mode			
TE NEWORY		Standard	<u>(</u> v)	
BOOTING				
DIPUT/OUTPUT				
NE H/W MONITOR				
🤆 VANAGENENT				F4: Save & Exit Setup
E CHIPSET	6			
SECURITY	Profiles	SUPERMICR		Save & Los

The following items will be displayed:

• Fan 1 Speed ~ Fan 5 Speed

Fan Speed Control Mode

This feature allows the user to decide how the system controls the speeds of the onboard fans. The CPU temperature and the fan speed are correlative. When the CPU on-die temperature increases, the fan speed will also increase for effective system cooling. Select "Full Speed" to allow the onboard fans to run at full speed (of 100% Pulse Width Modulation Duty Cycle) for maximum cooling. This setting is recommended for special system configuration or debugging. Select "Standard" for the onboard fans to run at 50% of the Initial PWM Cycle in order to balance the needs between system cooling and power saving. This setting is recommended for regular systems with normal hardware configurations. The options are Full Speed (@100% of PWM Cycle) and **Standard** (@50% of PWM Cycle).

Voltage

) SYSTEM INFORMATION		Fan Speed Voltage		
PROCESSOR/ CPU		: +1.384 V		
		: +12.302 V		
		: +1.280 V		
NENORY				
+ BOOTIING				
0001210				
DIPUT/OUTPUT				
a Depitouroi				
H/W MONITOR				
NANAGENENT				
			-	
E CHIPSET		and the second se	Contraction of the	
E UHUPSET				
		Community of the		
	Profiles	SUPERMICRO		Save & L
SECURITY				

The following items will be displayed:

- VCPU
- 12V
- VCPU_SA
- 5VCC
- VDIMM
- VCPU_IO
- VCPU_GT
- VDIMM_2.5
- PCH 1.0V
- 3.3V_DL
- VSB3
- 3.3VCC
- VBAT
- VCPU_STPLL

4-10 Management

Set all options for Management in this section.

ACPI Settings



ACPI Sleep State

This feature selects the ACPI Sleep State that the system will enter into when the suspend button is activated. The options are Suspend Disabled, and **S3 (Suspend to RAM)**.

Console Redirection

	ACPI Settings Console Redire	ection SuperFlash Utility		
PROCESSOR/ OPU	COM1 COM1 Console Redirection	(0 <u>1. ~</u>		
	Console Redirection Settings Serial Port for Out-of-Band Mar Windows Emergency Managem			
III VENORY	(EMS) EMS Console Redirection Console Redirection Settings			
BOOTING				
DIPUT/OUTPUT				
I HAY MONITOR				
🕸 WANAGEWENT				
±∰E CHEPSET				
SECURITY	Profiles	SUPERMIC	R•	Save & Load

COM 1

Console Redirection

Select Enabled to enable COM Port 1 Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are **Disabled (unchecked)** and Enabled (checked).

*If the item above set to Enabled, the following items will become available for configuration:

Console Redirection Settings

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this item to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/ VT100 terminals. The options are **Enabled (checked)** and Disabled (unchecked).

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are Enabled (checked) and **Disabled (unchecked)**.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are **Enabled (checked)** and Disabled (unchecked)..

Legacy OS Redirection Resolution

Use this item to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80×24 and 80×25 .

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS Post

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When the option-Bootloader is selected, legacy Console Redirection is disabled before booting the OS. When the option- Always Enable is selected, legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and Bootloader.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

Enable Console Redirection

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are Enabled (checked) and **Disabled (un-checked)**.

*If the item above set to Enabled, the following items will become available for user's configuration:

Console Redirection Settings

Out-of-Band Mgmnt Port

The feature selects a serial port in a client server to be used by the Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1 (Console Redirection)** and SOL (Console Redirection).

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, VT100+, and **VT-UTF8**.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in both host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop data-sending when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None,** Hardware RTS/CTS, and Software Xon/Xoff.

The setting for each these features is displayed:

Data Bits, Parity, Stop Bits

SuperFlash Utility

SYSTEM INFORMATION		ection SuperFlash Utility	
PROCESSOR/ CPU	*** Please Press TOP "SuperFlat Utility" to run < <on bios="" board="">></on>	sh SYSTEM Configure Se	
t overolocking	<un bius="" board="">>> BIOS Version : 1.0c BIOS Tag : 1088B Date : 12/9/2015</un>	N/A N/A N/A	
I VENCRY			
BOOTING			
DIPUT/OUTPUT			
N H/W MONITOR			
t Nantizeneni.			ESC: Exit
E CHIPSET	A		2
SECURITY	Profiles	SUPERMICR	Save & L

The following information will be displayed:

- **ON Board BIOS** displays the BIOS version.
- **Product** displays the product name.
- **BiosTag** displays the BIOS Tag number.
- **Date** displays the release date of the BIOS.
- **Time** displays the release time of the BIOS.
- 1. Click on the SuperFlash Utility feature at the top of the screen to enter the flash utility.
- 2. At the prompt, select "Yes" to reboot and configure the system to Flash mode. Select "No" to cancel and view the BIOS information.

TE NENORY	Date : 12/9/2015 Time : 9:27:40	N/A N/A	
	1. Press "Yes" S to configure s	perFlash!! stem will auto reboot stem to Flash mode. view information	
		Yes	
SECURITY		SUPERMICR	

3. After the system reboots to the flash mode, the system is ready to flash the BIOS. At the prompt, select "OK" to continue.

Start Flash			
Syste	em Ready for Flash!		
3.Waitti	Start Flash to Flash BIOS. ing for System.		
	on't flash BIOS, please select "Exit without Save"		
		_	
	SUPERMICR		
	DOI LAUIDO		

4. Select "Select File" and then in the pop-up menu select "General USB Flash Disk 1.00."

Select	File	
Select File Genera	USB Flash Disk 1.00	
	SUPERMICRO	

5. Select the filename ("C7Z170-SQ") in the pop-up menu.

Keep SMBIOS Data Select File Start Flash	.B	
<c7h17 <x105r <(7217 <x105r <(7117</x105r </x105r </c7h17 	70_1c+ PA_F> 10+SQ> PA	
<x105a <c7h17 <syster< td=""><td>NE> 10_1022> m Volume In> 20.2_v2></td><td></td></syster<></c7h17 </x105a 	NE> 10_1022> m Volume In> 20.2_v2>	
<c7x99 <c7x99< td=""><td></td><td></td></c7x99<></c7x99 		

6. Select "Start Flash" to flash the BIOS. A pop-up message will appear to show the progress of the BIOS flash.

Start Flash Update F	V MAIN	
Update FV_MAIN		
Update PV, MAIN Progress:		

 If the flash is successful, a pop-up message will indicate the result. Select "OK" to complete the BIOS flash and to reboot the system. Go to the "SYSTEM INFORMATION - Motherboard" page in the BIOS Setup to check for the correct BIOS version.

Flash		
	uccessed!	
	uccessed! ed! Please Cold Reset your system	
	nd Plesse Cold Reset your system	
	nd Plesse Cold Reset your system	
	nd Plesse Cold Reset your system	
	nd Plesse Cold Reset your system	
	nd Plesse Cold Reset your system	
	nd Plesse Cold Reset your system	

4-11 Chipset

Set all options for the Chipset in this section.

PROCESSOR/ CPU	System Agent Bri SA PCIe Code Vers			
to verolocking	VT-d VT-d	Supported		
III VENORY		(AUTO	(▼) (▼)	
+ BOOTING				
DIPUT/OUTPUT				
RE HAN NONITOR				
E CHIPSET				10

System Agent

The following will be displayed:

- **System Agent Bridge Name** this displays the System Agent bridge name.
- **System Agent RC Version** indicates the System Agent RC version.
- VT-d Capability this item indicates whether VT-d is supported.

VT-d

Select Enabled to enable Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to the Virtual Machine Manager (VMM) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The settings are **Enabled** and Disabled.

SW Guard Extensions (SGX)

Use this feature to enable or disable the Intel Software Guard Extensions (SGX). SGX is a set of CPU instructions that increases software security. The options are Disabled, **Enabled** and Software Controlled.

PRMRR Size

The BIOS must reserve a contiguous region of Processor Reserved Memory (PRM) in the Processor Reserved Memory Range Register (PRMRR). This item appears if SW Guard Extensions is enabled. The options are **Auto**, 32MB, 64MB and 128MB.

Above 4GB MMIO BIOS Assignment

Select Enable for remapping of BIOS above 4GB. The options are Enabled and **Disabled**.

Graphics

	-			
PROCESSOR/ OPU	GT - Power Management Co GT Info RC6(Render Standby)	licable		
to a construction and the second s		Auto Auto	(v) (v) (v)	
REE VENCRY				
+ BOOTING				
O INFUT/OUTPUT				
KAN NONITOR				
¢ VANLGEVENT				-ESC: Exit
👼 CHIPSET	1	\sim		
SECURITY	Profiles	SUPERM	IICRO	Save & Lo

The following will be displayed:

• **GT Info** - displays the Graphics chip information.

RC6 (Render Standby)

Use this feature enable Render Standby support. The options are $\ensuremath{\textbf{En-abled}}$ and Disabled.

Primary Display

Use this feature to select the graphics device to be used as the primary display. Select from IGFX/PEG/PCI or select SG for switchable GFX. The options are **Auto**, IGFX, PCH SLOT1 PCI-E 3.0 X1 (IN X4), CPU SLOT2 PCI-E 3.0 X4 (IN X16), PCH SLOT3 PCI-E 3.0 X4, CPU SLOT4 PCI-E 3.0 X8 (IN X16), PCH SLOT5 PCI-E 3.0 X1 (IN X4), and CPU SLOT6 PCI-E 3.0 X16.

Internal Graphics

This item keeps the IGD (Internal Graphics Device) enabled, based on setup options. The options are **Auto**, Enabled and Disabled.

PCH-IO

SYSTEM INFORMATION		S PCH-IO	No resident succession of the
PROCESSOR/ CPU		1.7.0.0 PCH-H Desktop Z170,5K	
7 OVEROLOOKING		31/D1 (Auto (*) Enubled (*)	
WENCRY		(Inabled (Y) (Enabled (Y)	
BOOTEN6			
DIPUT/OUTPUT			
RE HAVE NONETOR			
			ESC: Exit
CHIPSET	A	\sim	
SECURITY	Profiles	SUPERMICR	Save & Los

The following will be displayed:

- Intel PCH RC Version displays the PCH chip release version.
- Intel PCH SKU Name indicates the PCH chip part number.
- Intel PCH Rev ID displays the PCH chip revision ID.

HD Audio

This item controls the HD Audio device. The settings are **Auto**, Enabled and Disabled.

PCH LAN Controller

This item controls the PCH LAN Controller. The settings are **Enabled** and Disabled.

Wake on LAN

Select Enabled to enable the capability to 'wake-up' the system through the Ethernet port. The settings are **Enabled** and Disabled.

PCIE PLL SSC

This item enables the PCIE PLL SSC function. The settings are Enabled and **Disabled.**

4-12 Security

	Passwerd Secure Boot
PROCESSOR/ OPU	Password Desc
	then this only limits. Mores to Setup and is only asked for when entering Setup. If ONLY the User's password is set, then this
IIII NENORY	is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights.
+ BOOTIING	nere Auministration rights. The password length must be in the following range: Minimum length 3
DIPUT/OUTPUT	
NE H/W MONITOR	Password Check (Internet Page F3: Load Optimized Default Default Environment Page F3: Load Optimized Default Environment F3: Kasare & Exit Setup
t numenbil	
E CHIPSET	
SECURITY	
	SUPERMICRO Copyright (C) 2015 American Megatrond

Password

This menu allows the user to configure the following security settings for the system.

- If ONLY the Administrator password is defined this controls access to the BIOS setup ONLY.
- If the User's password is defined ONLY this password will need to be entered upon each system boot, and will also have Administrator rights in the setup.
- Passwords must be at least 3 and up to 20 characters long.

Administrator Password

Use this feature to set the Administrator Password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

User Password

Use this feature to set the User Password, which is required everytime the system boots. The length of the password should be from 3 characters to 20 characters long.

Password Check

Use this feature to select whether the motherboard prompts for a password always, or only when entering setup. The options are **Setup** and Always.

Secure Boot Menu

PROCESSOR/ CPU	Password Description	
	If ONLY the Administrator's password a s	
	then this only limits access to Setup and is	
S OVERCLOCKING	only asked for when entering Setup.	
	If ONLY the User's password is set, then this	
	is a power on password and must be entered to	
UENORY	boot or enter Setup. In Setup the User will	
	have Administrator rights.	
	The password length must be	
> BOOTING	in the following range:	
DIPUT/OUTPUT		
	User Password -Not Installed	
	Password Check (Setup (Y)	
R H/W MONITOR		
C WANTRENENL		
C- BARMACHERT		
E CHIPSET		

The following items will be displayed:

- System Mode indicates the current system mode.
- Secure Boot this item indicates if Secure Boot is activated or not.

Secure Boot

Select Enabled for Secure Boot flow control. This feature is available when the platform key (PK) is pre-registered, the platform operates in the user mode, and CSM is disabled in the Setup utility. The options are **Disabled** and Enabled.

Secure Boot Mode

This feature allows selection of the Secure Boot Mode between Standard and Custom. Selecting Custom enables users to change the Image Execution Policy and manage Secure Boot Keys. The options are **Custom** and Standard.

Key Management



(if Secure Boot Mode is set to 'Custom')

Key Management allows experienced users to modify Secure Boot Variables.



Provision Factory Default Keys

Select Enabled to install factory default Secure boot keys. The options are **Disabled** and Enabled.

Enroll Factory Default Keys

This option forces system into user mode, installing all factory default keys (PK, KEK, db, dbt, dbx). Changes will take effect after reboot. Select "Yes" when prompted, otherwise click "No".

Platform Key

This item displays the current Platform Key status. Clicking this option will bring up the Key Management window.

Set New Key

This item uploads and installs a secure Platform Key. You may insert a factory default key or load from a file. The file formats accepted are: 1) Public Key Certificate

- a. EFI Signature List
- b. EFI CERT X509 (DER Encoded)
- c. EFI CERT RSA2048 (bin)
- d. EFI SERT SHA256 (bin)
- 2) EFI Time Based Authenticated Variable

When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Delete Key

This item deletes a previously installed Platform Key.

Key Exchange Key

This item displays the current Key Exchange Key status. Clicking this option will bring up the Key Management window.

Set New Key

This item uploads and installs a Key Exchange Key. You may insert a factory default key or load from a file. When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Append Key

This item uploads and adds a Key Exchange Key into the Key Management. You may insert a factory default key or load from a file. When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Delete Key

This item deletes a previously installed Key Exchange Key.

Authorized Signatures

This item displays the current Authorized Signatures status. Clicking this option will bring up the Key Management window.

Set New Key

This item uploads and installs an Authorized Signature. You may insert a factory default key or load from a file. The file formats accepted are:

- 1) Public Key Certificate
- a. EFI Signature List
- b. EFI CERT X509 (DER Encoded)
- c. EFI CERT RSA2048 (bin)
- d. EFI SERT SHA256 (bin)
- 2) EFI Time Based Authenticated Variable

When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Append Key

This item uploads and adds an Authorized Signature into the Key Management. You may insert a factory default key or load from a file. When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Delete Key

This item deletes a previously installed Authorized Signature.

Forbidded Signatures

This item displays the current Forbidden Signatures status. Clicking this option will bring up the Key Management window.

Delete Key

This item deletes a previously installed Forbidden Signature.

Set New Key

This item uploads and installs a Forbidden Signature. You may insert a factory default key or load from a file. The file formats accepted are: 1) Public Key Certificate

- a. EFI Signature List
- b. EFI CERT X509 (DER Encoded)
- c. EFI CERT RSA2048 (bin)
- d. EFI SERT SHA256 (bin)
- 2) EFI Time Based Authenticated Variable

When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Append Key

This item uploads and adds an Forbidden Signature into the Key Management. You may insert a factory default key or load from a file. When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Authorized TimeStamps

This item displays the current Authorized TimeStamps status. Clicking this option will bring up the Key Management window.

Set New Key

This item uploads and installs a Forbidden Signature. You may insert a factory default key or load from a file. The file formats accepted are: 1) Public Key Certificate

- a. EFI Signature List
- b. EFI CERT X509 (DER Encoded)
- c. EFI CERT RSA2048 (bin)
- d. EFI SERT SHA256 (bin)
- 2) EFI Time Based Authenticated Variable

When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Append Key

This item uploads and adds an Forbidden Signature into the Key Management. You may insert a factory default key or load from a file. When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

Delete Key

This item deletes a previously installed Forbidden Signature.

4-13 Recovery

See Appendix D.

Notes

Appendix A

BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue with bootup. The error messages normally appear on the screen.

Fatal errors will not allow the system to continue to bootup. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list correspond to the number of beeps for the corresponding error.

A-1 BIOS Error Beep Codes

В	BIOS Error Beep Codes			
Beep Code/LED	Error Message	Description		
1 beep	Refresh	Circuits have been reset. (Ready to power up)		
5 short beeps + 1 long beep	Memory error	No memory detected in the system		
5 short beeps	Display error	System display error		
OH LED On	System OH	System Overheat		

Notes

Appendix B

Software Installation Instructions

B-1 Installing Drivers

After you've installed the Windows Operating System, a screen as shown below will appear. You are ready to install software programs and drivers that have not yet been installed. To install these software programs and drivers, click the icons to the right of these items. (**Note**: To install the Windows Operating System, please refer to the instructions posted on our website at http://www.supermicro.com/support/manuals/.)



Driver/Tool Installation Display Screen

Note 1. Click the icons showing a hand writing on the paper to view the readme files for each item. Click on a computer icon to the right of an item to install this item (from top to the bottom), one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

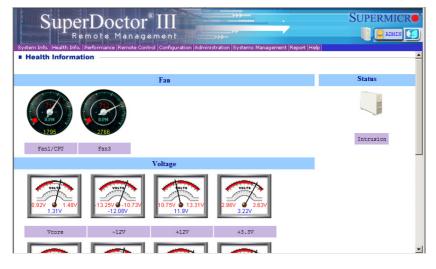
Note 2. When making a storage driver diskette by booting into a Driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

B-2 Configuring SuperDoctor[®] III

The SuperDoctor III program is a Web-based management tool that supports remote management capability. It includes Remote and Local Management tools. The local management tool is called the SD III Client. The SuperDoctor III program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. SuperDoctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the Figure below for a display of the SuperDoctor III interface.

Note: 1 The default user name and password are ADMIN.

Note 2: In the Windows OS environment, the SuperDoctor III settings take precedence over the BIOS settings. When first installed, SuperDoctor III adopts the temperature threshold settings previously set in BIOS. Any subsequent changes to these thresholds must be made within SuperDoctor, since the settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SD III Client settings to be the same as those set in BIOS.



SuperDoctor III Interface Display Screen-I (Health Information)



SuperDoctor III Interface Display Screen-II (Remote Control)

Note: The SuperDoctor III software and manual may be downloaded from our Website at:

http://www.supermicro.com/products/accessories/software/Super-DoctorIII.cfm.

For Linux, we still recommend that you use SuperDoctor II, this version is also available for download at the link above.

Notes

Appendix C

UEFI BIOS Recovery Instructions

Attention! Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

C-1 An Overview to the UEFI BIOS

The Unified Extensible Firmware Interface (UEFI) specification provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot up the system. UEFI offers a clean, hands-off control to a computer system at bootup.

C-2 How to Recover the UEFI BIOS Image (-the Main BIOS Block)

A UEFI BIOS flash chip consists of a recovery BIOS block, comprised of two boot blocks and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is on, the boot block codes execute first. Once that is completed, the main BIOS code will continue with system initialization and bootup.

Note: Follow the BIOS Recovery instructions below for BIOS recovery when the main BIOS boot crashes. However, when the BIOS boot block crashes, you will need to follow the procedures in Appendix D.

C-3 To Recover the Main BIOS Block Using a USB-Attached Device

This feature allows the user to recover a BIOS image using a USBattached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\" Directory of a USB device or a writeable CD/DVD.



Note: If you cannot locate the "SUPER.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device (save in the root folder) and rename it "SUPER.ROM" for BIOS recovery use.

- Insert the USB device that contains the new BIOS image ("SUPER.ROM") into any available USB port Be sure the file is saved in the very top (root) folder.
- Set the JBR1 switch on the motherboard to recovery mode and power on the system. If the screen appears as below, press to continue.

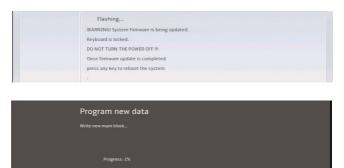
Version 2.16.1243. Copyright (C) 2013 American Megatrends, Inc. Supermicro C7237-05C BIOS Date:04/25/2014 Revi1.0 CPU : Intel(R) Core(TW) 17-4765T CPU 0 2.000Hz Speed : 2.00 GHz The IMC is operating with DORSL 1333 MHz Setup default has been loaded. Press (AEL) to Continue Booting 4. After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.

Recovery
ROM Image update allowed
ROM Image Verification passed
Flash Update Parameters
Reset NVRAM : 🖌 🖌
Boot Block Update : ✔
Proceed with flash update



Note: At this point, you may decide if you want to start with BIOS Recovery. If you decide to proceed with BIOS Recovery, follow the procedures below.

 To continue with BIOS Recovery, select the item- "Proceed with flash update". You will see the progress of BIOS Recovery as shown on the screens below.





Note: <u>Do not interrupt</u> the BIOS programming until it is completed.

6. After the BIOS Recovery process is complete, click OK to reboot the system.



Notes

Appendix D

Dual Boot Block

D-1 Introduction

This motherboard supports the Dual Boot Block feature, which is the lastditch mechanism to recover the BIOS boot block. This section provides an introduction to the feature.

BIOS Boot Block

A BIOS boot block is the minimum BIOS loader required to enable necessary hardware components for the BIOS crisis recovery flash that will update the main BIOS block. An on-call BIOS boot-block corruption may occur due to a software tool issue (see image below) or an unexpected power outage during BIOS updates.

AMI Firmware Update Utility vX.XX.XX Copyright (C)XXXX American Megatrends Inc. All Rights Reserved. Reading flash done -- ME Data Size checking . ok -- FFS checksums ok Erasing Boot Block done __Updating Boot Block 0x00A91000 (13%)

BIOS Boot Block Corruption Occurrence

When a BIOS boot block is corrupted due to an unexpected power outage or a software tool malfunctioning during BIOS updates, you can still reboot the system by activating switch JBR1 on the motherboard. When JBR1 is activated, the system will boot from a backup boot block preloaded in the BIOS by the manufacturer.

D-2 Steps to Reboot the System by switch JBR1

- 1. Power down the system.
- 2. On switch JBR1 slide switch to ON, and power on the system.
- 3. Follow the BIOS recovery SOP listed in the previous chapter (Appendix C).
- 4. After completing the steps above, power down the system.
- 5. Turn OFF switch JBR1, and power on the system.

(Disclaimer Continued)

The products sold by Supermicro are not intended for and will not be used in life support systems, medical equipment, nuclear facilities or systems, aircraft, aircraft devices, aircraft/emergency communication devices or other critical systems whose failure to perform be reasonably expected to result in significant injury or loss of life or catastrophic property damage. Accordingly, Supermicro disclaims any and all liability, and should buyer use or sell such products for use in such ultra-hazardous applications, it does so entirely at its own risk. Furthermore, buyer agrees to fully indemnify, defend and hold Supermicro harmless for and against any and all claims, demands, actions, litigation, and proceedings of any kind arising out of or related to such ultra-hazardous use or sale.